

## WIDE BAND DPDT SWITCH

### DESCRIPTION

The  $\mu$ PD5738T6N is a CMOS MMIC DPDT (Double Pole Double Throw) switch which is developed for mobile communications, wireless communications and another RF switching applications.

This device can operate within frequency from 0.01 to 2.5 GHz, having low insertion loss and high isolation performances. This device is housed in a 6-pin plastic TSON (Thin Small Out-line Non-leaded) (T6N) package, which allows high-density surface mounting.

### FEATURES

- Supply voltage :  $V_{DD} = 1.5$  to  $3.6$  V (2.8 V TYP.)
- Switch control voltage :  $V_{cont(H)} = 1.5$  to  $3.6$  V (2.8 V TYP.)  
:  $V_{cont(L)} = -0.2$  to  $+0.4$  V (0 V TYP.)
- Low insertion loss <sup>Note</sup> :  $L_{ins1} = 0.5$  dB TYP. @  $f = 0.01$  to  $0.05$  GHz  
:  $L_{ins2} = 0.8$  dB TYP. @  $f = 0.05$  to  $1.0$  GHz  
:  $L_{ins3} = 1.4$  dB TYP. @  $f = 1.0$  to  $2.0$  GHz  
:  $L_{ins4} = 1.6$  dB TYP. @  $f = 2.0$  to  $2.5$  GHz
- High isolation <sup>Note</sup> :  $ISL1 = 45$  dB TYP. @  $f = 0.01$  to  $0.05$  GHz  
:  $ISL2 = 22$  dB TYP. @  $f = 0.05$  to  $1.0$  GHz  
:  $ISL3 = 16$  dB TYP. @  $f = 1.0$  to  $2.0$  GHz  
:  $ISL4 = 15$  dB TYP. @  $f = 2.0$  to  $2.5$  GHz
- Handling power <sup>Note</sup> :  $P_{in(1dB)} = +20$  dBm TYP. @  $f = 1.0$  GHz  
:  $P_{in(0.1dB)} = +15$  dBm TYP. @  $f = 1.0$  GHz
- High-density surface mounting: 6-pin plastic TSON (T6N) package ( $1.5 \times 1.5 \times 0.37$  mm)
- High ESD voltage : machine-model 200 V (TYP.), human-body-model 3 kV (TYP.)

**Note**  $T_A = 25^\circ\text{C}$ ,  $V_{DD} = 2.8$  V,  $V_{cont(H)} = 2.8$  V,  $V_{cont(L)} = 0$  V

### APPLICATIONS

- Mobile communications
- Wireless communications
- Another RF switching applications

### ORDERING INFORMATION

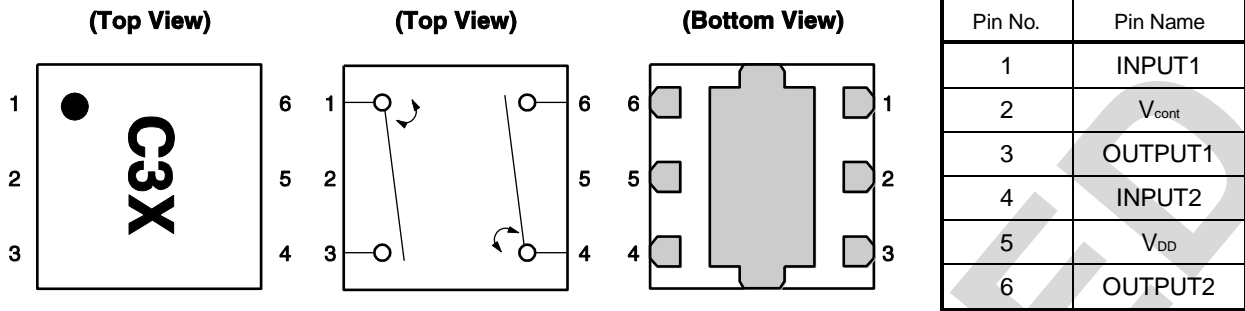
Part Number	Order Number	Package	Marking	Supplying Form
$\mu$ PD5738T6N-E2	$\mu$ PD5738T6N-E2-A	6-pin plastic TSON (T6N) (Pb-Free)	C3X	<ul style="list-style-type: none"> <li>• Embossed tape 8 mm wide</li> <li>• Pin 1, 6 face the perforation side of the tape</li> <li>• Qty 3 kpcs/reel</li> </ul>

**Remark** To order evaluation samples, please contact your nearby sales office.  
Part number for sample order:  $\mu$ PD5738T6N-A

**Caution: Observe precautions when handling because these devices are sensitive to electrostatic discharge**

The information in this document is subject to change without notice. Before using this document, please confirm that this is the latest version.

**PIN CONNECTIONS AND INTERNAL BLOCK DIAGRAM**



Remark Exposed pad : GND

**TRUTH TABLE**

V <sub>cont</sub>	INPUT1-OUTPUT1, INPUT2-OUTPUT2	INPUT1-OUTPUT2, INPUT2-OUTPUT1
Low	ON	OFF
High	OFF	ON

Remark High: +2.8 V, Low: 0 V

**ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub> = +25°C, unless otherwise specified)**

Parameter	Symbol	Ratings	Unit
Supply Voltage	V <sub>DD</sub>	-0.5 to +4.6	V
Switch Control Voltage	V <sub>cont</sub>	-0.5 to +4.6	V
Voltage Difference	V <sub>cont (H)</sub> - V <sub>DD</sub>	+0.5	V
Input Power	P <sub>in</sub>	+23	dBm
Operating Ambient Temperature	T <sub>A</sub>	-45 to +85	°C
Storage Temperature	T <sub>stg</sub>	-55 to +150	°C

**RECOMMENDED OPERATING RANGE (T<sub>A</sub> = +25°C, unless otherwise specified)**

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Supply Voltage	V <sub>DD</sub>	+1.5	+2.8	+3.6	V
Switch Control Voltage (H)	V <sub>cont (H)</sub>	+1.5	+2.8	+3.6	V
Switch Control Voltage (L)	V <sub>cont (L)</sub>	-0.2	0	+0.4	V

Remark  $V_{DD} - 0.4 V \leq V_{cont (H)} \leq V_{DD} + 0.2 V$

**ELECTRICAL CHARACTERISTICS**

( $T_A = +25^\circ\text{C}$ ,  $V_{DD} = 2.8\text{ V}$ ,  $V_{\text{cont(H)}} = 2.8\text{ V}$ ,  $V_{\text{cont(L)}} = 0\text{ V}$ ,  $P_{\text{in}} = 0\text{ dBm}$ ,  $Z_0 = 50\ \Omega$ , DC blocking capacitors = 10 000 pF, unless otherwise specified)

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Insertion Loss 1	L <sub>ins1</sub>	f = 0.01 to 0.05 GHz	–	0.5	0.9	dB
Insertion Loss 2	L <sub>ins2</sub>	f = 0.05 to 1.0 GHz	–	0.8	1.2	dB
Insertion Loss 3	L <sub>ins3</sub>	f = 1.0 to 2.0 GHz	–	1.4	1.8	dB
Insertion Loss 4	L <sub>ins4</sub>	f = 2.0 to 2.5 GHz	–	1.6	2.0	dB
Isolation 1	ISL1	f = 0.01 to 0.05 GHz	35	45	–	dB
Isolation 2	ISL2	f = 0.05 to 1.0 GHz	18	22	–	dB
Isolation 3	ISL3	f = 1.0 to 2.0 GHz	13	16	–	dB
Isolation 4	ISL4	f = 2.0 to 2.5 GHz	12	15	–	dB
Return Loss 1	RL1	f = 0.01 to 1.0 GHz	13	18	–	dB
Return Loss 2	RL2	f = 1.0 to 2.5 GHz	8	12	–	dB
0.1 dB Loss Compression Input Power <sup>Note 1</sup>	P <sub>in(0.1 dB)</sub>	f = 1.0 GHz	+10	+15	–	dBm
1 dB Loss Compression Input Power <sup>Note 2</sup>	P <sub>in(1 dB)</sub>	f = 1.0 GHz	–	+20	–	dBm
Supply Current	I <sub>DD</sub>	V <sub>DD</sub> = V <sub>cont</sub> = 2.8 V, RF off	–	0.01	1	μA
Switch Control Current	I <sub>cont</sub>	V <sub>DD</sub> = V <sub>cont</sub> = 2.8 V, RF off	–	0.01	1	μA
Switch Control Speed	t <sub>sw</sub>	f = 1.0 GHz	–	0.4	1	μs

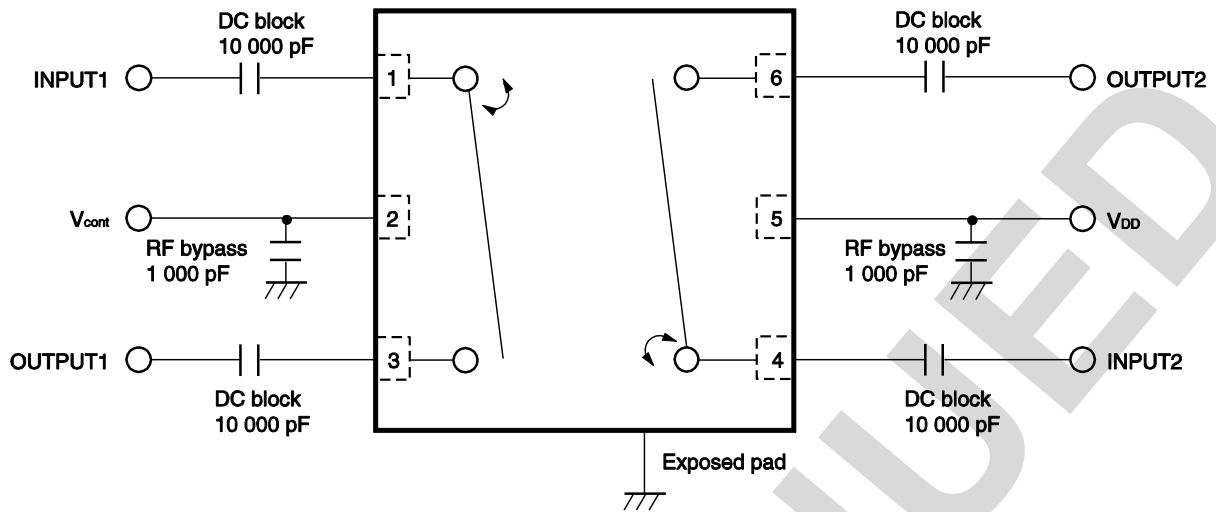
**Notes 1.** P<sub>in(0.1 dB)</sub> is measured the input power level when the insertion loss increases more 0.1 dB than that of linear range.

**2.** P<sub>in(1 dB)</sub> is measured the input power level when the insertion loss increases more 1 dB than that of linear range.

**Caution** DC blocking capacitors are necessary. Please do not supply any DC bias to the terminals (INPUT1, INPUT2, OUTPUT1, OUTPUT2).

The value of DC blocking capacitors should be chosen to accommodate the frequency of operation, bandwidth, switching speed and the condition with actual board of your system.

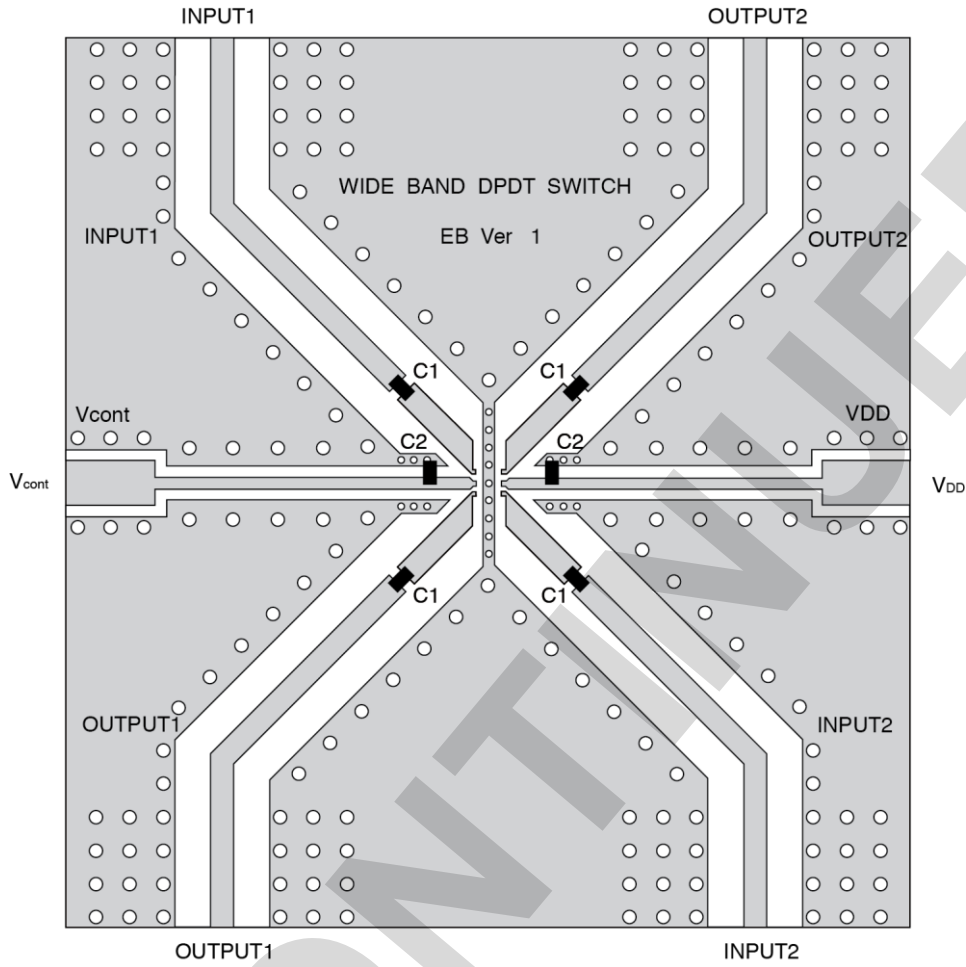
EVALUATION CIRCUIT



**Caution** This IC has pull down resistances inside between each RF line and GND line, which bias each RF pin internally to GND, then the IC cannot be used for DC switching.

The application circuits and their parameters are for reference only and are not intended for use in actual design-ins.

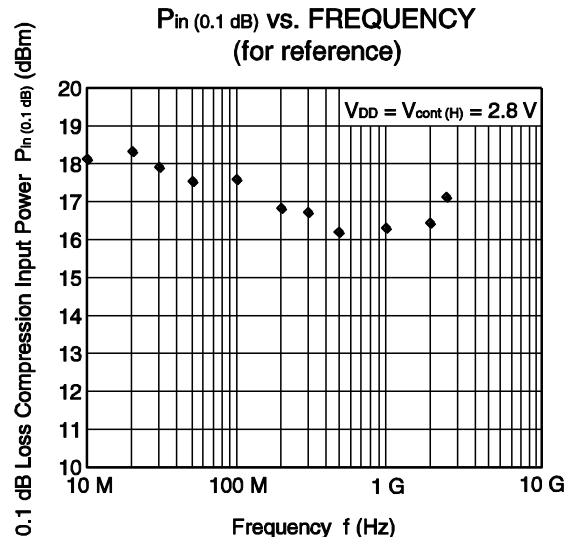
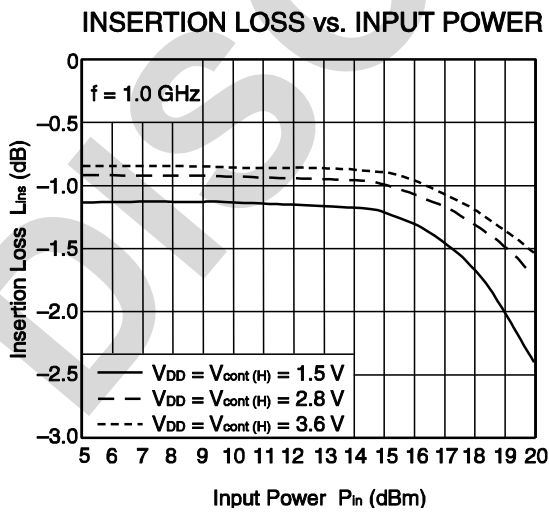
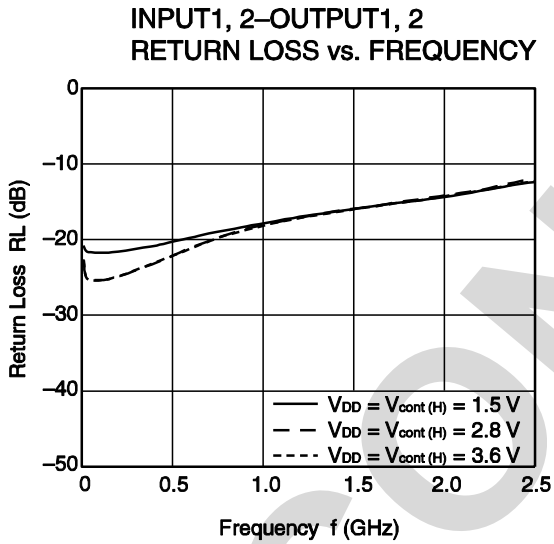
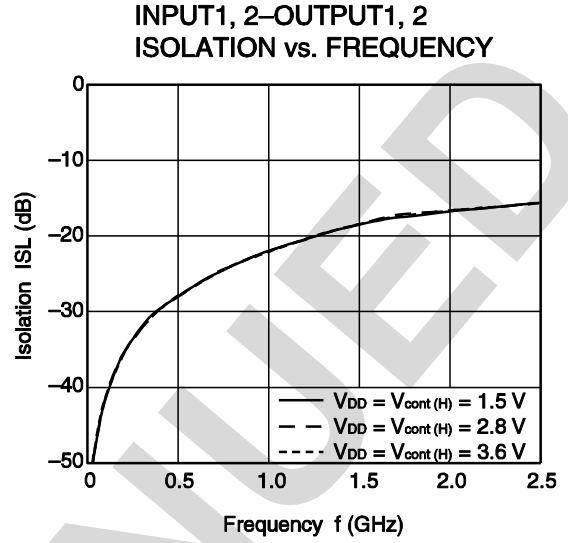
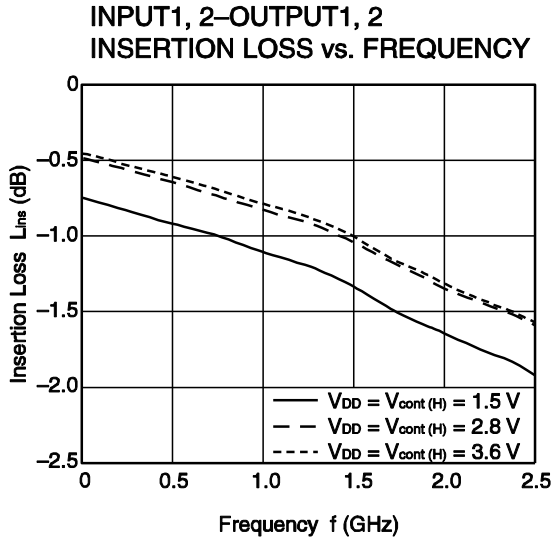
ILLUSTRATION OF THE TEST CIRCUIT ASSEMBLED ON EVALUATION BOARD



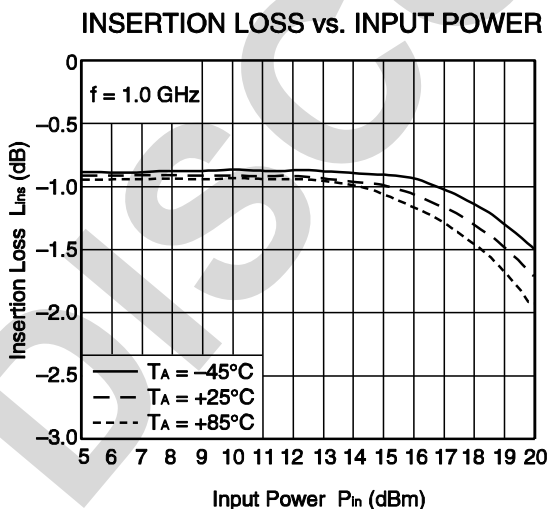
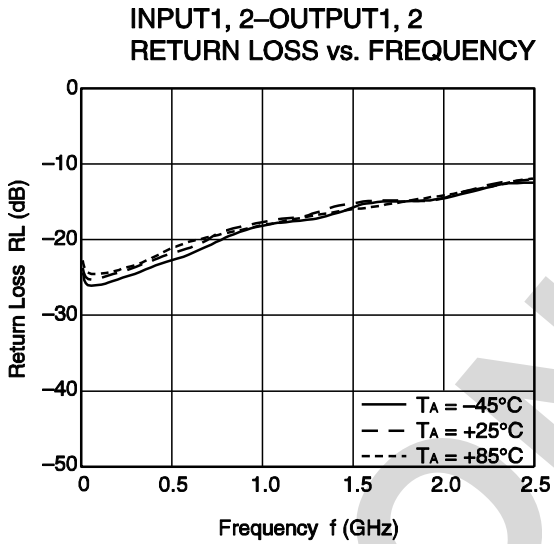
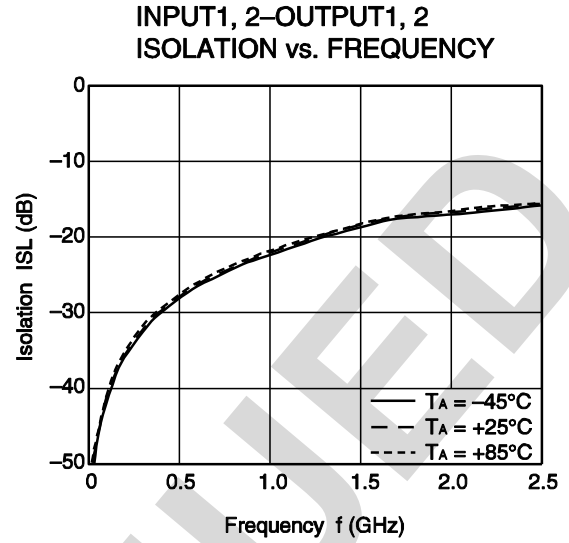
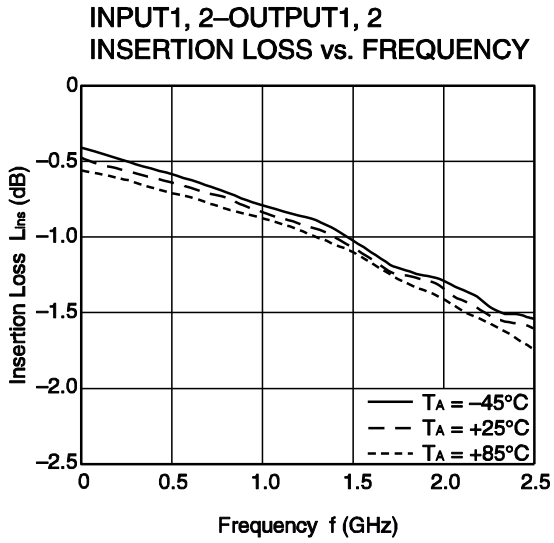
USING THE EVALUATION BOARD

Symbol	Values
C1	1 0000 pF
C2	1 000 pF

TYPICAL CHARACTERISTICS ( $T_A = +25^\circ\text{C}$ ,  $V_{DD} = 2.8\text{ V}$ ,  $V_{cont(H)} = 2.8\text{ V}$ ,  $V_{cont(L)} = 0\text{ V}$ ,  $P_{in} = 0\text{ dBm}$ ,  $Z_0 = 50\ \Omega$ , DC blocking capacitors = 10 000 pF, unless otherwise specified)



Remark The graphs indicate nominal characteristics.



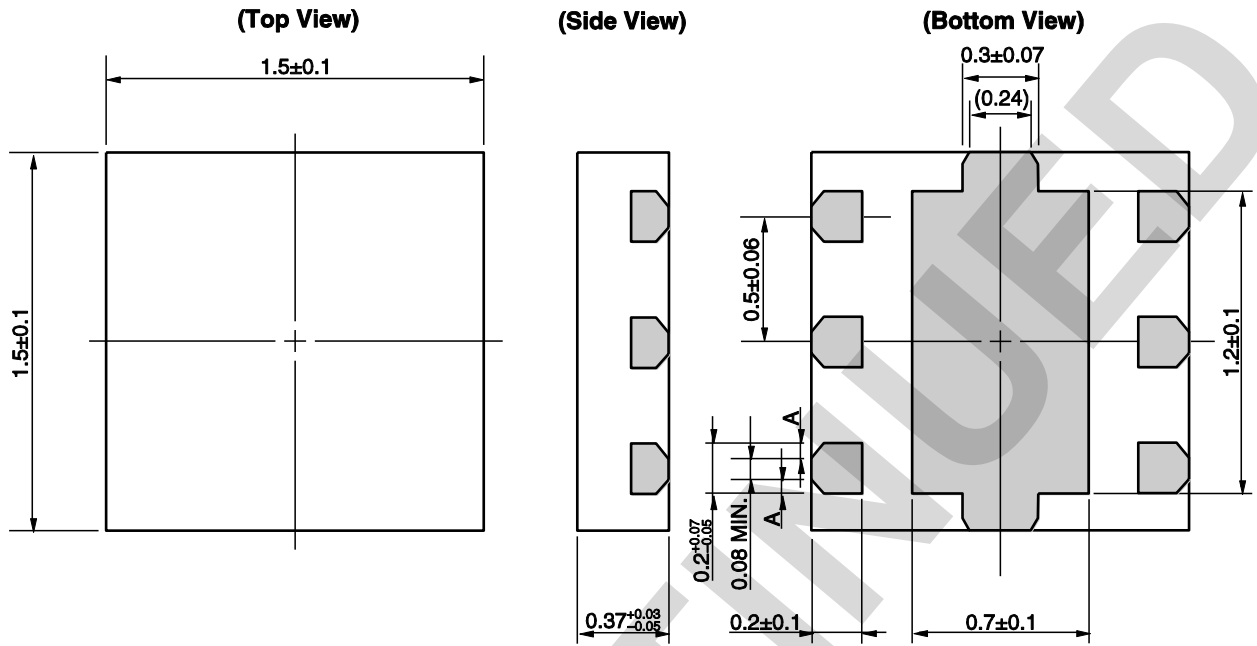
**Remark** The graphs indicate nominal characteristics.





PACKAGE DIMENSIONS

6-PIN PLASTIC TSON (T6N) (UNIT: mm)



Remark A>0

( ) : Reference value

DISCONTINUED

**RECOMMENDED SOLDERING CONDITIONS**

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions	Condition Symbol
Infrared Reflow	Peak temperature (package surface temperature) : 260°C or below Time at peak temperature : 10 seconds or less Time at temperature of 220°C or higher : 60 seconds or less Preheating time at 120 to 180°C : 120±30 seconds Maximum number of reflow processes : 3 times Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below	IR260
Partial Heating	Peak temperature (terminal temperature) : 350°C or below Soldering time (per side of device) : 3 seconds or less Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below	HS350

**Caution Do not use different soldering methods together (except for partial heating).**

DISCONTINUED

## NOTICE

1. Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation of these circuits, software, and information in the design of your equipment. California Eastern Laboratories and Renesas Electronics assumes no responsibility for any losses incurred by you or third parties arising from the use of these circuits, software, or information.
2. California Eastern Laboratories has used reasonable care in preparing the information included in this document, but California Eastern Laboratories does not warrant that such information is error free. California Eastern Laboratories and Renesas Electronics assumes no liability whatsoever for any damages incurred by you resulting from errors in or omissions from the information included herein.
3. California Eastern Laboratories and Renesas Electronics do not assume any liability for infringement of patents, copyrights, or other intellectual property rights of third parties by or arising from the use of Renesas Electronics products or technical information described in this document. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of California Eastern Laboratories or Renesas Electronics or others.
4. You should not alter, modify, copy, or otherwise misappropriate any Renesas Electronics product, whether in whole or in part. California Eastern Laboratories and Renesas Electronics assume no responsibility for any losses incurred by you or third parties arising from such alteration, modification, copy or otherwise misappropriation of Renesas Electronics product.
5. Renesas Electronics products are classified according to the following two quality grades: "Standard" and "High Quality". The recommended applications for each Renesas Electronics product depends on the product's quality grade, as indicated below. "Standard": Computers; office equipment; communications equipment; test and measurement equipment; audio and visual equipment; home electronic appliances; machine tools; personal electronic equipment; and industrial robots etc. "High Quality": Transportation equipment (automobiles, trains, ships, etc.); traffic control systems; anti-disaster systems; anti-crime systems; and safety equipment etc. Renesas Electronics products are neither intended nor authorized for use in products or systems that may pose a direct threat to human life or bodily injury (artificial life support devices or systems, surgical implantations etc.), or may cause serious property damages (nuclear reactor control systems, military equipment etc.). You must check the quality grade of each Renesas Electronics product before using it in a particular application. You may not use any Renesas Electronics product for any application for which it is not intended. California Eastern Laboratories and Renesas Electronics shall not be in any way liable for any damages or losses incurred by you or third parties arising from the use of any Renesas Electronics product for which the product is not intended by California Eastern Laboratories or Renesas Electronics.
6. You should use the Renesas Electronics products described in this document within the range specified by California Eastern Laboratories, especially with respect to the maximum rating, operating supply voltage range, movement power voltage range, heat radiation characteristics, installation and other product characteristics. California Eastern Laboratories shall have no liability for malfunctions or damages arising out of the use of Renesas Electronics products beyond such specified ranges.
7. Although Renesas Electronics endeavors to improve the quality and reliability of its products, semiconductor products have specific characteristics such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Further, Renesas Electronics products are not subject to radiation resistance design. Please be sure to implement safety measures to guard them against the possibility of physical injury, and injury or damage caused by fire in the event of the failure of a Renesas Electronics product, such as safety design for hardware and software including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures. Because the evaluation of microcomputer software alone is very difficult, please evaluate the safety of the final products or systems manufactured by you.
8. Please contact a California Eastern Laboratories sales office for details as to environmental matters such as the environmental compatibility of each Renesas Electronics product. Please use Renesas Electronics products in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. California Eastern Laboratories and Renesas Electronics assume no liability for damages or losses occurring as a result of your noncompliance with applicable laws and regulations.
9. Renesas Electronics products and technology may not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable domestic or foreign laws or regulations. You should not use Renesas Electronics products or technology described in this document for any purpose relating to military applications or use by the military, including but not limited to the development of weapons of mass destruction. When exporting the Renesas Electronics products or technology described in this document, you should comply with the applicable export control laws and regulations and follow the procedures required by such laws and regulations.
10. It is the responsibility of the buyer or distributor of California Eastern Laboratories, who distributes, disposes of, or otherwise places the Renesas Electronics product with a third party, to notify such third party in advance of the contents and conditions set forth in this document, California Eastern Laboratories and Renesas Electronics assume no responsibility for any losses incurred by you or third parties as a result of unauthorized use of Renesas Electronics products.
11. This document may not be reproduced or duplicated in any form, in whole or in part, without prior written consent of California Eastern Laboratories.
12. Please contact a California Eastern Laboratories sales office if you have any questions regarding the information contained in this document or Renesas Electronics products, or if you have any other inquiries.

**NOTE 1:** "Renesas Electronics" as used in this document means Renesas Electronics Corporation and also includes its majority-owned subsidiaries.

**NOTE 2:** "Renesas Electronics product(s)" means any product developed or manufactured by or for Renesas Electronics.

**NOTE 3:** Products and product information are subject to change without notice.

**CEL Headquarters** • 4590 Patrick Henry Drive, Santa Clara, CA 95054 • Phone (408) 919-2500 • [www.cel.com](http://www.cel.com)

For a complete list of sales offices, representatives and distributors,  
Please visit our website: [www.cel.com/contactus](http://www.cel.com/contactus)