

## TX-IF SIMMIC FOR W-CDMA ACG + I/Q MODULATOR

#### **FEATURES**

- TX-IF: 570 MHz
- LOW POWER CONSUMPTION: VCC = 3.0 V
- SMALL 20 PIN QFN PACKAGE: Flat lead style for better performance
- TAPE AND REEL PACKAGING AVAILABLE

#### DESCRIPTION

NEC's UPC8191K is a Silicon Microwave Monolithic Integrated Circuit designed as a transmitter/TX section for W-CDMA. The UPC8191K is a TX-IF IC including IF-AGC amplifier and modulator. This IC is suitable for kit-use for W-CDMA IF section.

This IC was developed using NEC's new ultra high seed silicon bipolar process.

NEC's stringent quality assurance and test procedures ensure the highest reliability and perormance.

#### 13 12 15 14 11 Reg & AGC\_CTRL 9 8 frea. 1/2 freg 1/2 7 Phase 6 Shifter 2 3 4 5

**UPC8191K** 

**APPLICATIONS** 

W-CDMA

| ELECTRICAL CHARACTERISTICS (unless oth                     | nerwise specified,TA = 25°C, Vcc = 3.0 V, fIF = 570 MHz, fLO = 760 MHz, |
|--|---|
| PI O = -15 dBm fl/Q = 10 kHz 400 mVp-p balanced sine-wave) |   |

|           | PART NUMBER<br>PACKAGE OUTLINE   | UPC8191K<br>QFN-20 |          |            |          |
|-----------|--|--------------------|----------|------------|----------|
| SYMBOLS   | PARAMETERS AND CONDITIONS  | UNITS              | MIN      | ТҮР        | MAX      |
| lcc       | Circuit Current, No Signals<br>At power saving mode  | mA<br>μA           |          | 30.5<br>0  | 38<br>1  |
| Ρουτ      | Output Power, VCONT= 2.3 V, I/Q = 400mVp-p balanced<br>VCONT= 0.3 V, I/Q = 400mVp-p balanced | dBm                | -17<br>_ | -13<br>-93 | _<br>-88 |
| LoL       | Local Leakage  | dBc                | -        | _          | -30      |
| lmR       | Image Rejection  | dBc                | -        | _          | -30      |
| Hm1       | Output Harmonics 1, Leakage when IF output = 190 MHz   | dBc                | -        | _          | -20      |
| Hm2       | Output Harmonics 2, Leakage when IF output = 380 MHz   | dBc                | -        | _          | -30      |
| TPS(Rise) | Rise time from power-saving mode   | us                 | -        | _          | 10       |
| VPS(Rise) | Rising voltage from power-saving mode  | V                  | 2.2      | _          | -        |
| VPS(fall) | Falling voltage from power-saving mode   | V                  | _        | _          | 0.5      |

### $\label{eq:standard} \textbf{STANDARD CHARACTERISTICS FOR REFERENCE} (unless otherwise specified, TA = 25^{\circ}\text{C}, \ Vcc = 3.0 \ V, \\ \textbf{V}_{\text{C}} = \textbf{V}_{\text{C}} =$

fIF = 570 MHz, fLO = 760 MHz, PLO = -15 dBm, fI/Q =10 kHz, 400 mVp-p balanced sine-wave)

| PART NUMBER<br>PACKAGE OUTLINE |  |        | UPC8191K<br>QFN-20 |      |     |
|--------------------------------|--|--------|--------------------|------|-----|
| SYMBOLS                        | PARAMETERS AND CONDITIONS                      | UNITS  | MIN                | ТҮР  | MAX |
| NFL1                           | Output Noise Level 1,Pout = -25 dBm, fIF±20MHz | -      | -148               | -    |     |
| NFL2                           | Output Noise Level 2,Pout = -65 dBm, fIF±20MHz | dBm/Hz | _                  | -162 | -   |
| GF                             | Gain Flatness, fIF±2.5MHz                      | dB     | _                  | -    | 0.5 |
| EVM                            | Error Vector Magnitude, I/Q = 3.84 Msps QPSK   | %rms   | _                  | 3    | -   |
| ACPR                           | Adjacent Channel Power Ratio, fIF±5 MHz        | dBc    | _                  | -55  | _   |

## California Eastern Laboratories

#### BLOCK DIAGRAM

### ABSOLUTE MAXIMUM RATINGS<sup>1</sup> (TA = 25°C)

| SYMBOLS    | PARAMETERS                       | UNITS | RATINGS          |
|------------|----------------------------------|-------|------------------|
| Vcc        | Supply Voltage                   | V     | 4.0              |
| VPS, VCONT | Applied Voltage                  | V     | -0.3 to Vcc +0.3 |
| Та         | Operating Ambient<br>Temperature | °C    | -40 to +85       |
| Тѕтс       | Storage Temperature              | °C    | -55 to +150      |

Notes:

1. Operation in excess of any one of these parameters may result in permanent damage.

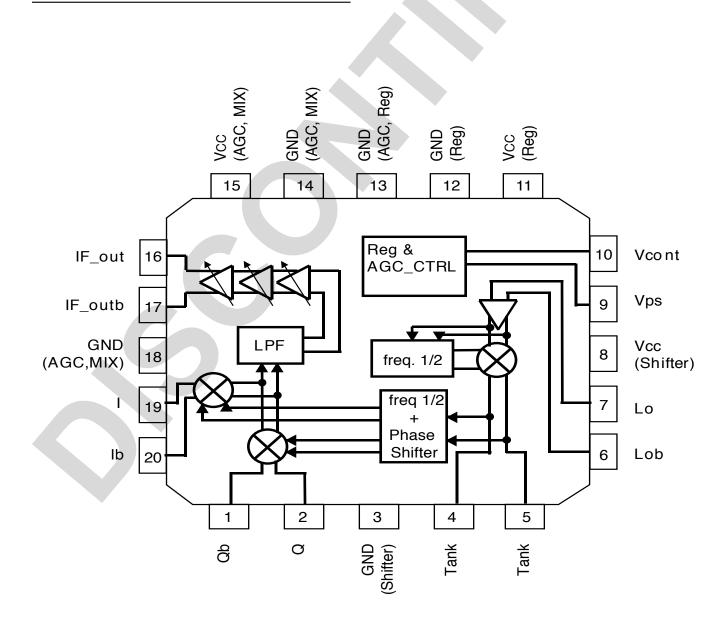
#### RECOMMENDED OPERATING CONDITIONS

| SYMBOLS | PARAMETERS   | UNITS | MIN | ТҮР | MAX |
|---------|--|-------|-----|-----|-----|
| Vcc     | Supply Voltage   | V     | 2.7 | 3.0 | 3.3 |
| TA      | Operating Ambient<br>Temperature                               | °C    | -25 | +25 | +85 |
| fIF     | IF Frequency   | MHz   | -   | 570 | -   |
| flo     | Local Frequency  | MHz   | 1   | 760 | -   |
| Plo     | Local input Level  | dBm   | -18 | -15 | -12 |
| ZIF     | IF output impedance,<br>Balanced output internal<br>resistance | kΩ    | -   | 1   | -   |
| VI/Q    | I/Q Maximum Input<br>Voltage (balanced input)                  | Vp-p  |     | 0.4 | 1   |

#### **ORDERING INFORMATION**

| Part Number   | Package            |
|---------------|--------------------|
| UPC8191K-E1-A | 20 Pin plastic QFN |

### BLOCK DIAGRAM (Units in mm)



| Pin      | Pin              | Applied<br>Voltage | Pin<br>Voltage | Functions and Applications   | Internal Equivalent Circuits  |
|----------|------------------|--------------------|----------------|--|---|
| No.<br>1 | Name<br>Qb       | (V)<br>VCC/2       | (V)<br>-       | Q signal input pin.  |   |
|          |                  |                    |                | Apply bias voltage externally.<br>Maximum balance input voltage is<br>1 000 mVP-P (balance).   |   |
| 2        | Q                | VCC/2              | -              |  |   |
| 3        | GND<br>(Shifter) | 0                  | -              | Ground pin of I/Q modulator.<br>This pin should be grounded with<br>minimum inductance.<br>Form the ground pattern as widely as<br>possible to minimize ground impedance.        |   |
| 4<br>5   | Tank             | 0                  | 2.65           | External inductor and capacitor can<br>supress harmonics spurious of LO frequency.<br>LC value should be determined<br>according to LO input frequency<br>and suppression level. | 4 5<br>External   |
| 6        | LOb              | 0                  | 2.02           | Bypass pin of local signal input for I/Q<br>modulator.<br>In the case of single local input,<br>this pin must be decoupled with capacitor<br>ex. 1 000 pF.                       | _   |
| 7        | LO               | 0                  | 2.02           | Local signal input of I/Q modulator.<br>The DC cut capacitor ex. 1 000 pF<br>must be attaced to this pin.  | _   |
| 8        | VCC<br>(Shifter) | 2.7 to 3.3         | •              | Supply voltage pin of I/Q modulator.   |   |
| 9        | VPS              | 0 to 3.0           | -              | Power saving pin of I/Q modulator +<br>AGC amplifier.  |   |
|          |                  |                    |                | This pin modulator can control<br>Active/Sleep state with bias as follows.   | 50 kΩ   |
|          |                  |                    |                | VPS (V) State  | ()<br>()<br>()<br>()<br>()<br>()<br>()<br>()<br>()<br>()<br>()<br>()<br>()<br>( |
|          |                  |                    |                | 0 to 0.5 Sleep Mode  | <b>▼</b>  |
|          |                  |                    |                |  |   |

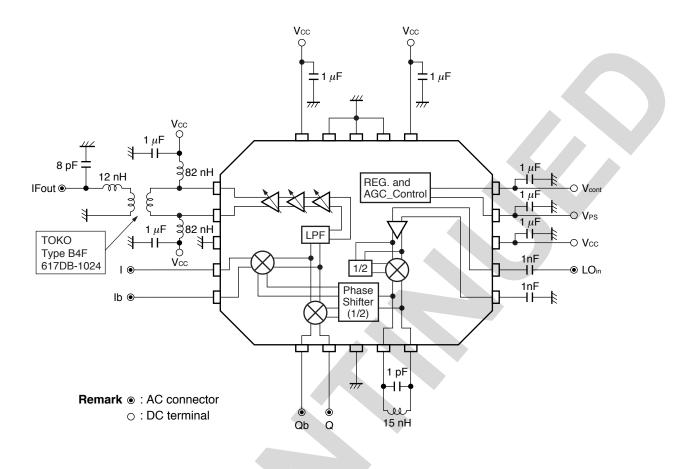
#### PIN FUNCTIONS (Pin Voltage is measured at Vcc = 2.85 V)

#### UPC8191K

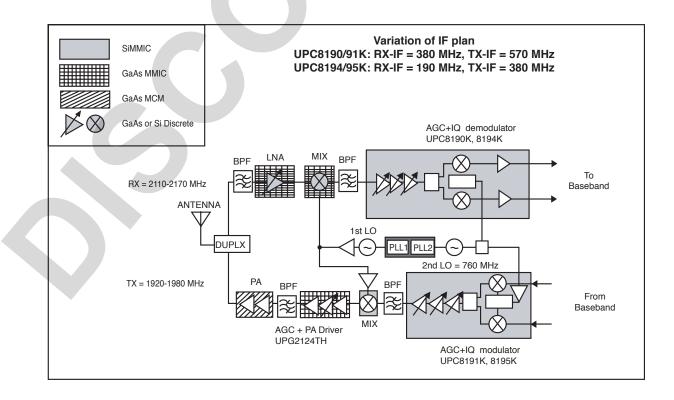
#### Applied Pin Functions and Applications Internal Equivalent Circuits Pin Pin Voltage Voltage Name No. (V) (V) 0 to 3.0 10 Vcont Gain control pin of AGC amplifier. \_ 10.5 kΩ Variable gains are available in accordance with applied voltage between 0 to 3.0 V. 2.5 kΩ 11 VCC 2.7 to 3.3 Supply voltage pin of internal regulator. (REG.) GND 0 Ground pin internal regulator. 12 (REG.) This pin should be grounded with minimum inductance. Form the ground pattern as widely as possible to minimize ground impedance. 13 GND 0 Ground pin of AGC amplifier + I/Q Mixer. 14 (AGC, This pin should be grounded with MIX) 18 minimum inductance. Form the ground pattern as widely as possible to minimize ground impedance. VCC 2.7 to 3.3 Supply voltage pin of AGC amplifier + 15 \_ \_ (AGC I/Q Mixer. , MIX) 2.7 to 3.3 16 IFout IF output pin. The inductor must be attached between External VCC and output pin due to open collector. Output frequency is 570 MHz which is 6 1 kΩ 3/4 of local signal frequency 760 MHz. 17 IFoutb 2.7 to 3.3 Balance output of IFout pin. The inductor must be attached between VCC and output pin due to open collector. VCC/2 19 I signal input pin. Т Apply bias voltage externally. Maximum balance input voltage is 1 000 mVP-P (balance). 20 20 lb VCC/2 .

#### PIN FUNCTIONS (Pin Voltage is measured at Vcc = 2.85 V)

#### MEASUEMENT CIRCUIT (Units in mm)

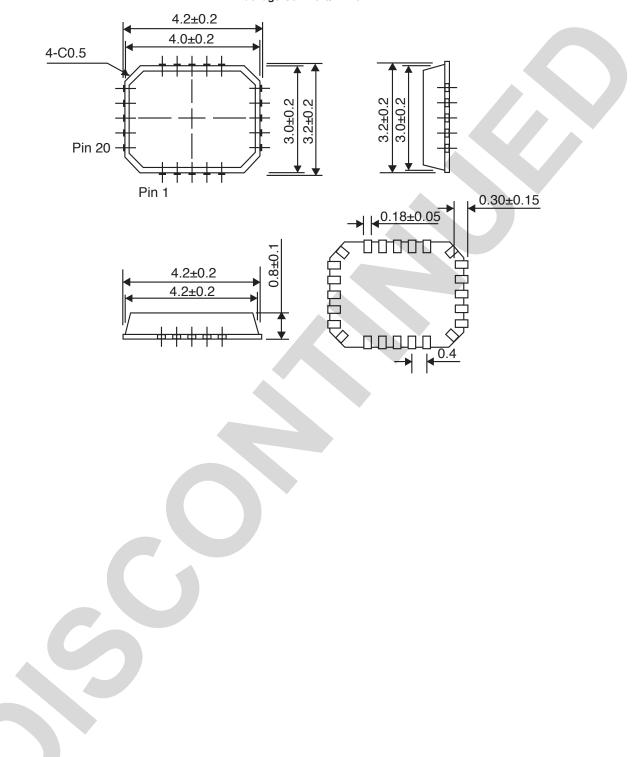


#### **APPLICATION EXAMPLE: W-CDMA**



#### OUTLINE DIMENSIONS (Units in mm)

Package Outline QFN-20



#### Life Support Applications

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This status is based on CEL's understanding of the EU Directives and knowledge of the materials that go into its products as of the date of disclosure of this information.

| Restricted Substance per RoHS | Concentration Limit per RoHS Concentration (values are not yet fixed) |                    |            |  |
|-------------------------------|---|--------------------|------------|--|
| Lead (Pb)                     | < 1000 PPM  | -A<br>Not Detected | -AZ<br>(*) |  |
| Mercury                       | < 1000 PPM  | Not Detected       |            |  |
| Cadmium                       | < 100 PPM   | Not Detected       |            |  |
| Hexavalent Chromium           | < 1000 PPM  | Not Detected       |            |  |
| РВВ                           | < 1000 PPM  | Not Detected       |            |  |
| PBDE                          | < 1000 PPM  | Not Detected       |            |  |

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