

S-5842A Series

DUAL TRIP TEMPERATURE SWITCH IC (THERMOSTAT IC)

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Rev.2.3_02

The S-5842A Series is a dual trip temperature switch IC (thermostat IC) which detects two points of temperature. The S-5842A Series operates at the power supply voltage of 2.5 V and the lower current consumption of 10 μ A typ. A temperature sensor with the negative temperature coefficient, a reference voltage generation circuit, a comparator and a delay circuit are integrated on one chip, and enclosed into the packages SOT-23-6 and SNT-6A.

Features

- Detection temperature:
- Accuracy of detection temperature:
- Low voltage operation:
- Low current consumption:
- Selectable output form:
- Selectable output type:
- Selectable output logic:
- Operation temperature range:
- Lead-free, Sn 100%, halogen-free*1

Detection temperature = -10° C to $+110^{\circ}$ C, $+1^{\circ}$ C step $\pm 2.5^{\circ}$ C (Only for either detection temperature) $V_{DD} = 2.5$ V min. (Detection temperature = $+20^{\circ}$ C to $+110^{\circ}$ C) $V_{DD} = 2.7$ V min. (Detection temperature = 0° C to $+110^{\circ}$ C) $V_{DD} = 2.8$ V min. (Detection temperature = -10° C to $+110^{\circ}$ C) $I_{DD} = 10 \ \mu$ A typ. (Ta = $+25^{\circ}$ C) CMOS output, Nch open-drain output Separate, Integrate "H", "L" Ta = -40° C to $+125^{\circ}$ C

*1. Refer to " Product Name Structure" for details.

Applications

- Fan control
- Air-conditioning system
- Mobile phone
- Game console
- Various electronics devices

Packages

- SOT-23-6
- SNT-6A

Block Diagrams

1. CMOS output product (Output type: Separate)

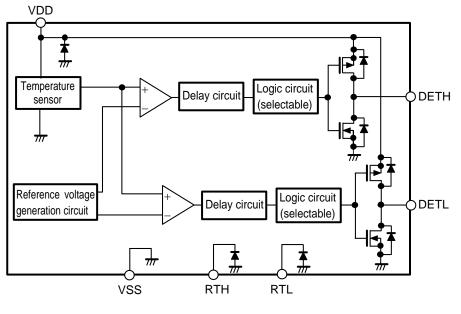


Figure 1

2. Nch open-drain output product (Output type: Separate)

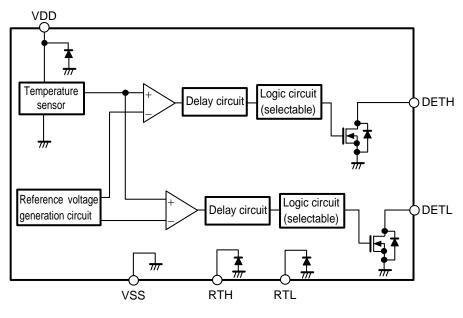
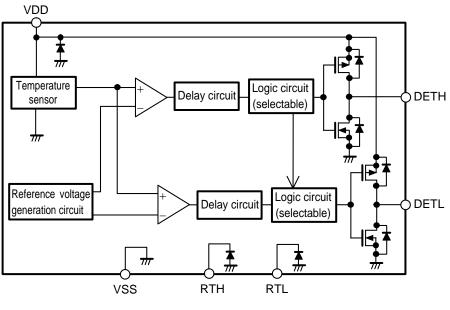


Figure 2

3. CMOS output product (Output type: Integrate)





4. Nch open-drain output product (Output type: Integrate)

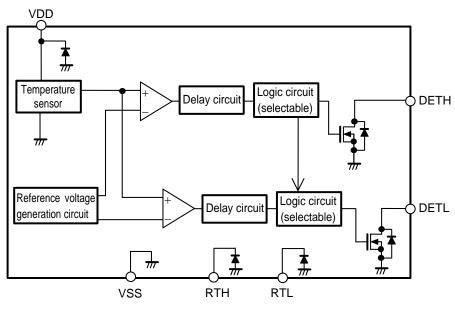


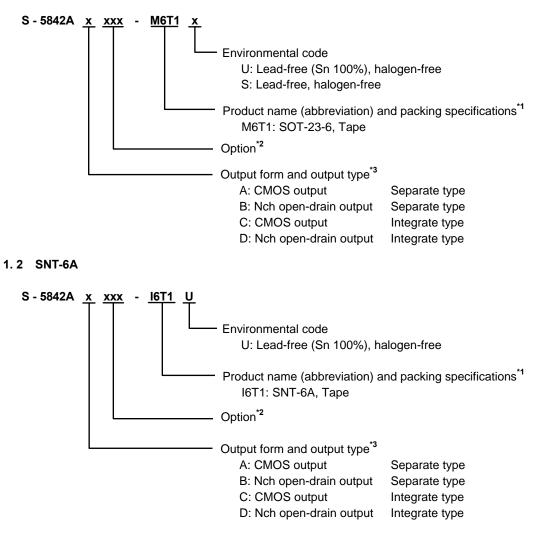
Figure 4

Product Name Structure

Users are able to select the output form and type, option for detection temperature's spec and package for the S-5842A Series.

1. Product name





- ***1.** Refer to the tape drawing.
- *2. Refer to " Selection of Product Option". Settings are available in alphabetical order as AAA to ZZZ.
- *3. Refer to " Selection of Product Option".

2. Packages

Table 1	Package	Drawing	Codes
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Package Name	Dimension	Таре	Reel	Land
SOT-23-6	MP006-A-P-SD	MP006-A-C-SD	MP006-A-R-SD	_
SNT-6A	PG006-A-P-SD	PG006-A-C-SD	PG006-A-R-SD	PG006-A-L-SD

3. Product name list

3.1 SOT-23-6

Table 2

		Option for Detection Temperature's Spec							
		DETH	Pin (for Hig	her Tempera	ture)	DETL	Pin (for Low	er Temperat	ure)
Product Name	Output Form and Type	Detection Temperature (T _{DH})	Accuracy of Detection Temperature ^{*1}	Hysteresis Temperature (Т _{нуѕн})	Output Logic	Detection Temperature (T _{DL})	Accuracy of Detection Temperature ^{*1}	Hysteresis Temperature (T _{HYSL})	Output Logic
S-5842AAAAF-M6T1y	CMOS, Separate	+95°C	_	−5°C	"L"	+55°C	±2.5°C	−5°C	"L"
S-5842AAAAG-M6T1y	CMOS, Separate	+60°C	_	−5°C	"H"	0°C	±2.5°C	+5°C	"L"
S-5842AAAAL-M6T1U	CMOS, Separate	+45°C	±2.5°C	−2°C	"H"	0°C	_	+2°C	"H"
S-5842AAAAN-M6T1y	CMOS, Separate	+50°C	_	−5°C	"L"	+40°C	±2.5°C	−5°C	"L"
S-5842ACAAM-M6T1y	CMOS, Integrate	+45°C	_	−2°C	"L"	0°C	±2.5°C	−2°C	"L"

*1 Either of two detection temperatures (T_{DH} , T_{DL}), an accuracy of ±2.5°C is set only for the one that user specifies a higher accuracy than the other (T_{DH} , T_{DL}).

Remark 1. Please contact our sales office for products other than those specified above. Refer to " Selection of Product Option" for details.

2. y: S or U

3. Please select products of environmental code = U for Sn 100%, halogen-free products.

DUAL TRIP TEMPERATURE SWITCH IC (THERMOSTAT IC) S-5842A Series

3.2 SNT-6A

Table 3

	Option for Detection Temperature's Spec									
		DETH	Pin (for High	ner Temperat	ture)	DETL Pin (for Lower Temperature)				
Product Name	Output Form and Type	Detection Temperature (T _{DH})	Accuracy of Detection Temperature ^{*1}	Hysteresis Temperature (Т _{нүsн})	Output Logic	Detection Temperature (T _{DL})	Accuracy of Detection Temperature ^{*1}	Hysteresis Temperature (T _{HYSL})	Output Logic	
S-5842AAAAL-I6T1U	CMOS, Separate	+45°C	±2.5°C	−2°C	"H"	0°C	_	+2°C	"H"	
S-5842AAAAT-I6T1U	CMOS, Separate	+45°C	_	−2°C	"H"	0°C	±2.5°C	+2°C	"L"	
S-5842ABAAA-I6T1U	Nch open-drain, Separate	+62°C	-	+2°C	"H"	−3°C	±2.5°C	−2°C	"L"	
S-5842ABAAC-I6T1U	Nch open-drain, Separate	+60°C	_	−2°C	"H"	0°C	±2.5°C	+2°C	"L"	
S-5842ABAAJ-I6T1U	Nch open-drain, Separate	+40°C	_	−2°C	"H"	+5°C	±2.5°C	+2°C	"L"	
S-5842ABAAP-I6T1U	Nch open-drain, Separate	+70°C	_	−5°C	"L"	+50°C	±2.5°C	−5°C	"H"	
S-5842ABAAV-I6T1U	Nch open-drain, Separate	+65°C	_	−5°C	"L"	+50°C	±2.5°C	−5°C	"L"	
S-5842ABAAW-I6T1U	Nch open-drain, Separate	+62°C	-	−2°C	"H"	+2°C	±2.5°C	−2°C	"H"	
S-5842ABAAX-I6T1U	Nch open-drain, Separate	+55°C	-	−5°C	"H"	-10°C	±2.5°C	+5°C	"L"	
S-5842ACAAS-I6T1U	CMOS, Integrate	+45°C	_	−2°C	"L"	0°C	±2.5°C	+2°C	"L"	
S-5842ADAAH-I6T1U	Nch open-drain, Integrate	+70°C	±2.5°C	−5°C	"L"	+5°C	-	−5°C	"H"	
S-5842ADAAK-I6T1U	Nch open-drain, Integrate	+60°C	±2.5°C	−5°C	"L"	+5°C	-	−5°C	"H"	
S-5842ADAAQ-I6T1U	Nch open-drain, Integrate	+60°C	-	−2°C	"H"	0°C	±2.5°C	+2°C	"L"	

*1 Either of two detection temperatures (T_{DH}, T_{DL}), an accuracy of ±2.5°C is set only for the one that user specifies a higher accuracy than the other (T_{DH}, T_{DL}).

Remark Please contact our sales office for products other than those specified above. Refer to " Selection of Product Option" for details.

Pin Configurations

1. SOT-23-6

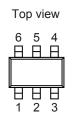


Figure 5

Pin No.	Symbol	Description
1	VDD	Power supply pin
2	DETL	Output pin for lower temperature detection
3	RTL ^{*1}	TEST pin
4	RTH ^{*2}	TEST pin
5	VSS	GND pin
6	DETH	Output pin for higher temperature detection

Table 4

*1. Set the RTL pin open in use.

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*2. Set the RTH pin open in use.

2. SNT-6A



Figure 6

Pin No.	Symbol	Description
1	DETH	Output pin for higher temperature detection
2	VSS	GND pin
3	RTH ^{*1}	TEST pin
4	RTL ^{*2}	TEST pin
5	DETL	Output pin for lower temperature detection
6	VDD	Power supply pin

Table 5

*1. Set the RTH pin open in use.

*2. Set the RTL pin open in use.

Absolute Maximum Ratings

Table 6

			(Ta = +25°C unless otherwise	specified)
	Item	Symbol	Absolute Maximum Rating	Unit
Power supply vo	oltage (V _{SS} = 0 V)	V _{DD}	V _{SS} + 6.5	V
Pin voltage		V_{RTH}, V_{RTL}	$V_{SS}-0.3$ to $V_{DD}+0.3$	V
	CMOS output product		$V_{SS}-0.3$ to $V_{DD}+0.3$	V
Output voltage Nch oper	Nch open-drain output product	V_{DETH}, V_{DETL}	$V_{\text{SS}}-0.3$ to $V_{\text{SS}}+6.5$	V
	nt	I _{OHH} , I _{OHL}	13	mA
Output pin curre		I _{OLH} , I _{OLL}	13	mA
Power	SOT-23-6	D	650 ^{*1}	mW
dissipation	SNT-6A	P _D	400 ^{*1}	mW
Operation ambie	ent temperature	T _{opr}	-40 to +125	°C
Storage tempera	ature	T _{stg}	-65 to +150	°C

*1. When mounted on board

[Mounted board]

(1) Board size : $114.3 \text{ mm} \times 76.2 \text{ mm} \times t1.6 \text{ mm}$

(2) Board name : JEDEC STANDARD51-7

Caution The absolute maximum ratings are rated values exceeding which the product could suffer physical damage. These values must therefore not be exceeded under any conditions.

DC Electrical Characteristics

1. CMOS output product

Table 7

				(Ta = +25°	°C unless othe	erwise s	pecified)
Item	Symbol	Condition	Min.	Тур.	Max.	Unit	Test Circuit
		Detection temperature = +20°C to +110°C	2.5	_	5.5	V	1
Power supply voltage	V _{DD}	Detection temperature = 0°C to +110°C	2.7	-	5.5	V	1
		Detection temperature = -10°C to +110°C	2.8	-	5.5	V	1
Detection temperature ^{*1}	$T_{DH} \text{ or } T_{DL}$	-	$T_{\text{DET}}-2.5$	T _{DET}	$T_{\text{DET}} + 2.5$	°C	1
Difference of detection temperature ^{*2}	ΔT_D	V_{DD} = 3.5 V	-	ΔT_{DET}	-	°C	1
Hysteresis temperature of DETH pin*3	T _{HYSH}	V _{DD} = 3.5 V	-	-5, -2, +2, +5	_	°C	1
Hysteresis temperature of DETL pin ^{*3}	T _{HYSL}	V _{DD} = 3.5 V	-	-5, -2, +2, +5	_	°C	1
Output current "H" of DETH pin	I _{ОНН}	$V_{\text{DD}} = 3.5 \text{ V}, V_{\text{DETH}} = 3.0 \text{ V}$	0.5	3.2	-	mA	2
Output current "H" of DETL pin	I _{OHL}	V_{DD} = 3.5 V, V_{DETL} = 3.0 V	0.5	3.2	-	mA	2
Output current "L" of DETH pin	I _{OLH}	V_{DD} = 3.5 V, V_{DETH} = 0.5 V	0.5	3.0	-	mA	2
Output current "L" of DETL pin	I _{OLL}	V_{DD} = 3.5 V, V_{DETL} = 0.5 V	0.5	3.0	-	mA	2
Current consumption during operation	I _{DD}	V _{DD} = 3.5 V	_	10	16	μΑ	1

*1. T_{DET}: set value for detection temperature, T_{DH}: actual detection temperature for higher temperature,

 T_{DL} : actual detection temperature for lower temperature

Either of two detection temperatures (T_{DH} , T_{DL}), an accuracy of $\pm 2.5^{\circ}$ C is set only for the one that user specifies a higher accuracy than the other (T_{DH} , T_{DL}).

*2. ΔT_{DET}: set value for detection temperature for higher temperature – set value for detection temperature for lower temperature

 ΔT_{D} : actual difference of detection temperature Set two points of detection temperature so that the difference of detection temperature (ΔT_{DET}) is in 0°C to 65°C. Users are able to set ΔT_{DET} in 0°C, 5°C, 10°C ... 60°C, 65°C.

*3. Users are able to select the hysteresis temperature in -5° C, -2° C, $+2^{\circ}$ C, or $+5^{\circ}$ C.

[Fahrenheit ⇔ Celsius Conversion equation]

 $^{\circ}C = (^{\circ}F - 32) \times 5 / 9$

 $^{\circ}F = 32 + ^{\circ}C \times 9 / 5$

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2. Nch open-drain output product

Table 8

				(Ta = +25°	C unless othe	erwise sp	ecified)
Item	Symbol	Condition	Min.	Тур.	Max.	Unit	Test Circuit
		Detection temperature = +20°C to +110°C	2.5	-	5.5	V	1
Power supply voltage	V _{DD}	Detection temperature = 0°C to +110°C	2.7	_	5.5	V	1
		Detection temperature = -10° C to $+110^{\circ}$ C	2.8	_	5.5	V	1
Detection temperature ^{*1}	T_{DH} or T_{DL}	_	$T_{\text{DET}}-2.5$	T _{DET}	$T_{DET} + 2.5$	°C	1
Difference of detection temperature ^{*2}	ΔT_D	V _{DD} = 3.5 V	—	ΔT_{DET}	_	°C	1
Hysteresis temperature of DETH pin ^{*3}	T _{HYSH}	V _{DD} = 3.5 V	_	-5, -2, +2, +5	-	°C	1
Hysteresis temperature of DETL pin*3	T _{HYSL}	V _{DD} = 3.5 V	_	-5, -2, +2, +5	-	°C	1
Leakage current of DETH pin	I _{LEAKH}	$V_{DD} = 3.5 \text{ V}, V_{DETH} = 5.5 \text{ V}$	—	Ι	100	nA	2
Leakage current of DETL pin	I _{LEAKL}	$V_{DD} = 3.5 \text{ V}, V_{DETL} = 5.5 \text{ V}$	-	-	100	nA	2
Output current "L" of DETH pin	I _{OLH}	$V_{DD} = 3.5 \text{ V}, V_{DETH} = 0.5 \text{ V}$	0.5	3.0	_	mA	2
Output current "L" of DETL pin	I _{OLL}	$V_{DD} = 3.5 \text{ V}, V_{DETL} = 0.5 \text{ V}$	0.5	3.0	_	mA	2
Current consumption during operation	I _{DD}	V _{DD} = 3.5 V	-	10	16	μA	1

*1. T_{DET}: set value for detection temperature, T_{DH}: actual detection temperature for higher temperature, T_{DL}: actual detection temperature for lower temperature

Either of two detection temperatures (T_{DH} , T_{DL}), an accuracy of $\pm 2.5^{\circ}$ C is set only for the one that user specifies a higher accuracy than the other (T_{DH} , T_{DL}).

*2. ΔT_{DET} : set value for detection temperature for higher temperature – set value for detection temperature for lower temperature

 ΔT_{D} : actual difference of detection temperature

Set two points of detection temperature so that the difference of detection temperature (ΔT_{DET}) is in 0°C to 65°C. Users are able to set ΔT_{DET} in 0°C, 5°C, 10°C ... 60°C, 65°C.

***3.** Users are able to select the hysteresis temperature in -5° C, -2° C, $+2^{\circ}$ C, or $+5^{\circ}$ C.

[Fahrenheit ⇔ Celsius Conversion equation]

 $^{\circ}C = (^{\circ}F - 32) \times 5 / 9$ $^{\circ}F = 32 + ^{\circ}C \times 9 / 5$

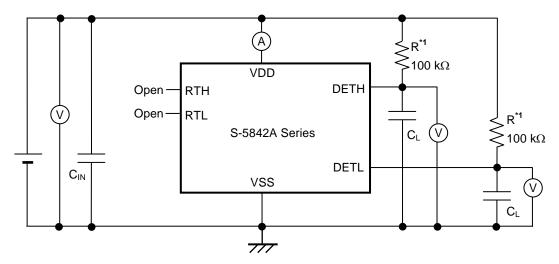
AC Electrical Characteristics

Table 9

Item	Symbol	Condition	Min.	Тур.	Max.	Unit	Test Circuit
Noise suppression time	t _{delay}	$V_{DD} = 3.5 V,$ Ta = detection temperature	-	550	_	μS	-

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Test Circuits



*1. Resistor (R) is unnecessary for the CMOS output product.

Figure 7 Test Circuit 1

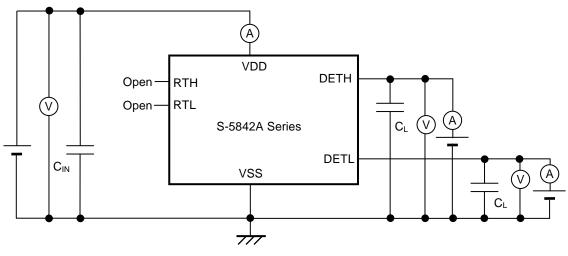


Figure 8 Test Circuit 2

Selection of Product Option

1. Description of product option

The S-5842A Series is a temperature switch IC (thermostat IC) which detects two points of temperature, and outputs a signal to the exterior.

Users are able to select the combination of output type, form and logic, two detection temperatures (T_{DH} , T_{DL}), and hysteresis temperature (T_{HYSH} , T_{HYSL}).

Output form: Set the output logic for each DETH pin and DETL pin.

- CMOS output
- Nch open-drain output

Output type: for DETH pin and DETL pin, users are able to select two types of output as follows.

- Separate type: The DETH pin's output inverts during detection of higher temperature The DETL pin's output inverts during detection of lower temperature
- Integrate type: The DETH pin's output inverts during detection of higher temperature

The DETL pin's output inverts during detection of higher and lower temperature

Detection temperature (T_{DH}, T_{DL}):

 T_{DH} is the detection temperature for higher temperature, T_{DL} is the detection temperature for lower temperature. Of two points of detection temperatures (T_{DH} , T_{DL}), select the detection temperature that is to be set the higher accuracy (±2.5°C accuracy).^{*1}

- The detection temperature for higher temperature is selectable in +20°C to +110°C, in 1°C step.
- The detection temperature for lower temperature is selectable in -10°C to +110°C, in 1°C step.
- Set two points of detection temperature so that the difference of detection temperature (ΔT_{DET}) is in 0°C to 65°C. Users are able to set ΔT_{DET} in 0°C, 5°C, 10°C ... 60°C, 65°C.

The minimum operation voltage varies according to the detection temperature for lower temperature.

• Detection temperature = +20°C to +110°C:	$V_{DD} = 2.5 V min.$
• Detection temperature = 0°C to +110°C:	$V_{DD} = 2.7 V min.$
• Detection temperature = -10°C to +110°C:	$V_{DD} = 2.8 V min.$

*1 Either of two detection temperatures (T_{DH}, T_{DL}), an accuracy of ±2.5°C is set only for the one that user specifies a higher accuracy than the other (T_{DH}, T_{DL}).

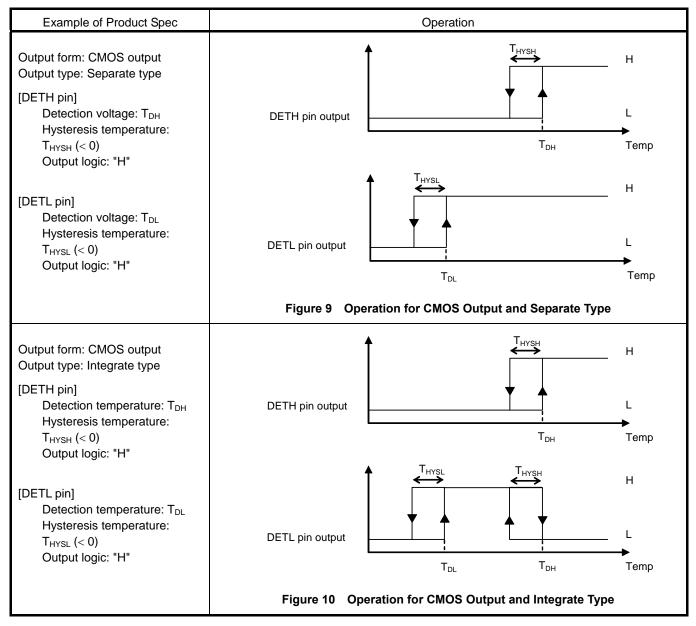
Hysteresis temperature (T_{HYSH} , T_{HYSL}):

 T_{HYSH} is the hysteresis temperature for detection temperature for higher temperature, T_{HYSL} is the hysteresis temperature for detection temperature for lower temperature. These are selectable in $-5^{\circ}C$, $-2^{\circ}C$, $+2^{\circ}C$, or $+5^{\circ}C$.

- -5°C: Output recovers when temperature has dropped to 5°C from the detection temperature.
- -2°C: Output recovers when temperature has dropped to 2°C from the detection temperature.
- +2°C: Output recovers when temperature has risen to 2°C from the detection temperature.
- +5°C: Output recovers when temperature has risen to 5°C from the detection temperature.

- (1) Hysteresis temperature: -5°C, -2°C
 - \bullet The output logic is "H" in Ta > detection temperature
 - The output logic is "L" in Ta > detection temperature
- (2) Hysteresis temperature: +2°C, +5°C
 - The output logic is "H" in Ta > release temperature
 - The output logic is "L" in Ta > release temperature

2. Example of product spec



Operation

1. Separate type

The followings are the operation with its specs when the S-5842A Series is Separate type. The timing chart is shown in **Figure 11**.

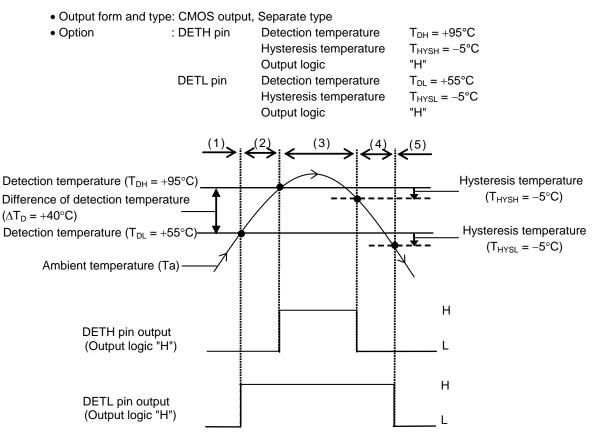


Figure 11 Timing Chart for CMOS Output and Separate Type

Detecting the temperature is started after power-on.

In case;

- (1) Ambient temperature (Ta) < detection temperature (T_{DL} = +55°C) The DETH pin output and the DETL pin output keep "L".
- (2) The ambient temperature (Ta) rises; detection temperature (T_{DL} = +55°C) < ambient temperature (Ta) < detection temperature (T_{DH} = +95°C) The DETH pin output keeps "L", the DETL pin output gets "H".
- (3) Furthermore, the ambient temperature (Ta) rises; ambient temperature (Ta) > detection temperature (T_{DH} = +95°C) The DETL pin output keeps "H", the DETH pin output gets "H".
- (4) After that, the ambient temperature (Ta) falls; release temperature ($T_{DH} + T_{HYSH} = +90^{\circ}C$) > ambient temperature (Ta) > release temperature ($T_{DL} + T_{HYSL} = +50^{\circ}C$) The DETH pin output gets "L", the DETL pin output keeps "H".
- (5) Furthermore the ambient temperature (Ta) falls;
 ambient temperature (Ta) < release temperature (T_{DL} + T_{HYSL} = +50°C)
 The DETH pin output keeps "L", the DETL pin output gets "L".

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2. Integrate type

The followings are the operation with its specs when the S-5842A Series is Separate type. The timing chart is shown in **Figure 12**.

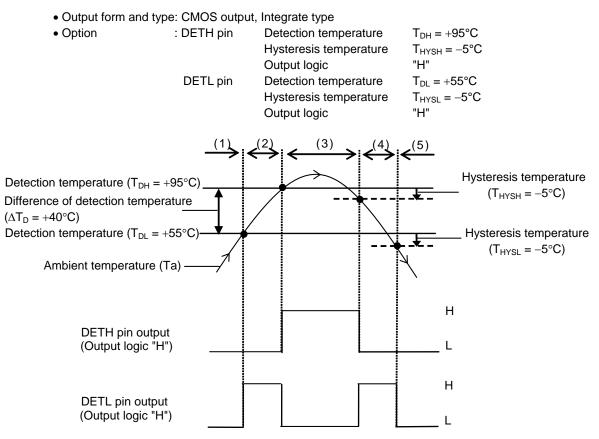


Figure 12 Timing Chart for CMOS Output and Integrate Type

Detecting the temperature is started after power-on.

In case;

- (1) Ambient temperature (Ta) < detection temperature (T_{DL} = +55°C) The DETH pin output and the DETL pin output keep "L".
- (2) The ambient temperature (Ta) rises; detection temperature (T_{DL} = +55°C) < ambient temperature (Ta) < detection temperature (T_{DH} = +95°C) The DETH pin output keeps "L", the DETL pin output gets "H".
- (3) Furthermore, the ambient temperature (Ta) rises;
 ambient temperature (Ta) > detection temperature (T_{DH} = +95°C)
 The DETH pin output gets "H", the DETL pin output gets "L".
- (4) After that, the ambient temperature (Ta) falls; release temperature $(T_{DH} + T_{HYSH} = +90^{\circ}C)$ > ambient temperature (Ta) > release temperature $(T_{DL} + T_{HYSL} = +50^{\circ}C)$ The DETH pin output gets "L", the DETL pin output gets "H".
- (5) Furthermore the ambient temperature (Ta) falls;
 ambient temperature (Ta) < release temperature (T_{DL} + T_{HYSL} = +50°C)
 The DETH pin output keeps "L", the DETL pin output gets "L".

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3. Delay circuit

The S-5842A Series sets the noise suppression time (t_{delay}) via the delay circuit. By this, the S-5842A Series prevents false detection operations of DETH pin and DETL pin output.

The followings are the operation of the DETH pin output when the output logic is "H".

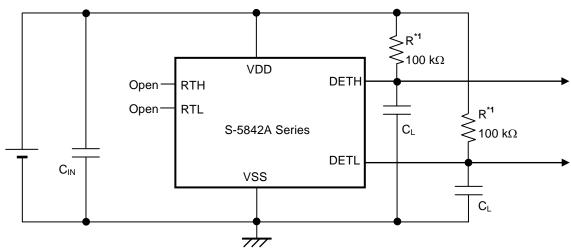
3.1 The temperature is the detection temperature or less

The output from a comparator is "H", and the DETH pin output is "L". Due to noise or others, the output from a comparator is inverted to "L" once; however, the DETH pin output keeps "L" if this status is t_{delay} or shorter.

3. 2 The temperature exceeds the detection temperature

The output from a comparator gets "L". And the DETH pin output gets "H" after the period has passed t_{delay} or longer.

Standard Circuit



*1. Resistor (R) is unnecessary for CMOS output product.

Figure 13

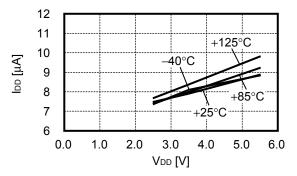
Caution The above connection diagram will not guarantee successful operation. Perform thorough evaluation using actual application to set the constant.

Precautions

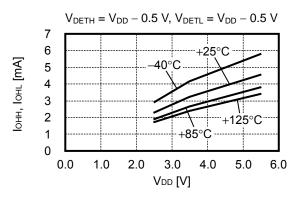
- Either of two detection temperatures (T_{DH}, T_{DL}), an accuracy of ±2.5°C is set only for the one that user specifies a higher accuracy than the other (T_{DH}, T_{DL}).
- Set a capacitor (C_{IN}) of 0.1 μ F or more between the VDD pin and VSS pin for stabilization.
- To prevent error due to noise during power-on, set a capacitor (C_L) of approx. 0.1 μF for the DETH pin and the DETL pin.
- The S-5842A Series may oscillate by connecting a capacitor to the RTH pin and the RTL pin. Set the RTH pin and the RTL pin open in use.
- The DETH / DETL pin output a signal that of (Ta > detection temperature) by short-circuit the RTH / RTL pin to VSS.
- Do not apply an electrostatic discharge to this IC that exceeds the performance ratings of the built-in electrostatic
 protection circuit.
- ABLIC Inc. claims no responsibility for any disputes arising out of or in connection with any infringement by products, including this IC, of patents owned by a third party.

Characteristics (Typical Data)

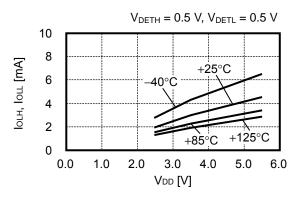
1. Current consumption during operation vs. Power supply voltage characteristics



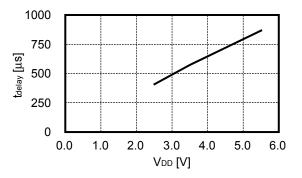
2. Output current "H" of DETH / DETL pin vs. Power supply voltage characteristics (CMOS output product only)



3. Output current "L" of DETH / DETL pin vs. Power supply voltage characteristics



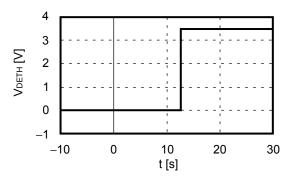
4. Noise suppression time vs. Power supply voltage characteristics



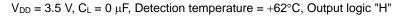
5. Response against heat (Output voltage vs. Time)

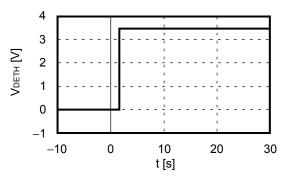
5. 1 When SNT-6A is put into the air of +100°C from the air of +25°C at t = 0 s

 V_{DD} = 3.5 V, C_L = 0 μ F, Detection temperature = +62°C, Output logic "H"



5. 2 When SNT-6A is put into the liquid of +100°C from the air of +25°C at t = 0 s

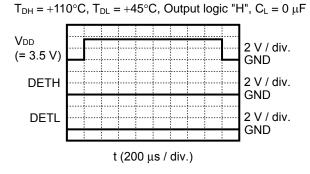




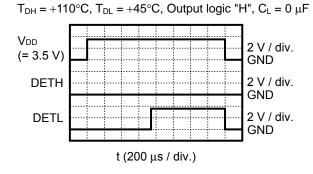
Rev.2.3_02

6. Response against startup

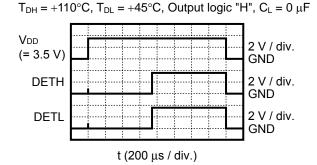
6. 1 Power-on at Ta = +25°C



6. 2 Power-on at Ta = +80°C



6. 3 Power-on at Ta = +120°C

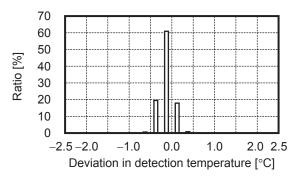


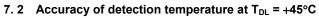
7. Accuracy of detection temperature

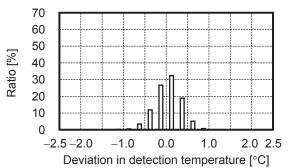
High accuracy detection voltage: T_{DH} $T_{DH} = +110^{\circ}C$, $T_{DL} = +45^{\circ}C$

Measured data on one wafer picked up

7.1 Accuracy of detection temperature at $T_{DH} = +110^{\circ}C$ (Higher accuracy)

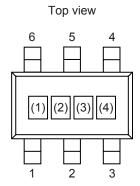






Marking Specifications

1. SOT-23-6



(1) to (3): (4): Product code (Refer to **Product name vs. Product code**) Lot number

Product name vs. Product code

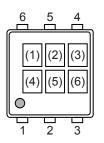
Product Name	Product Code					
Product Name	(1)	(2)	(3)			
S-5842AAAAF-M6T1y	U		F			
S-5842AAAAG-M6T1y	U	I	G			
S-5842AAAAL-M6T1U	U	I	L			
S-5842AAAAN-M6T1y	U	I	Ν			
S-5842ACAAM-M6T1y	U	J	М			

Remark 1. y: S or U

2. Please select products of environmental code = U for Sn 100%, halogen-free products.

2. SNT-6A

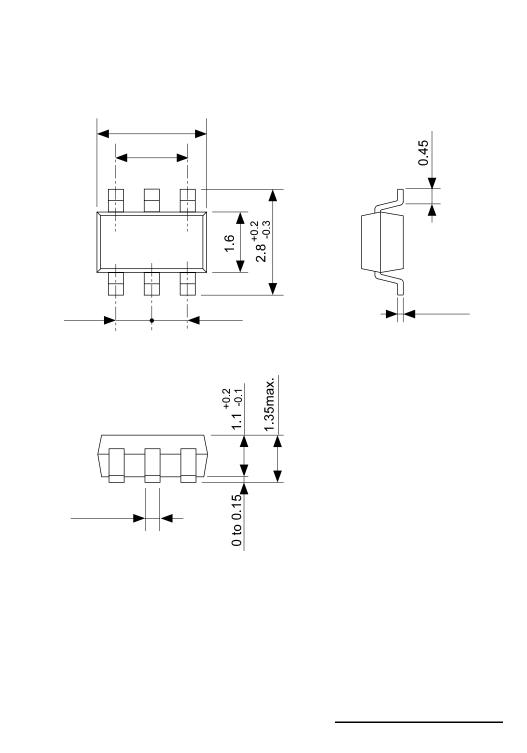
Top view

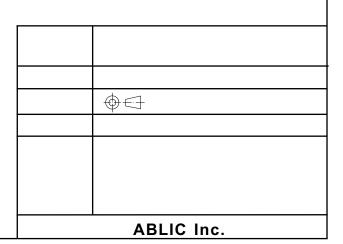


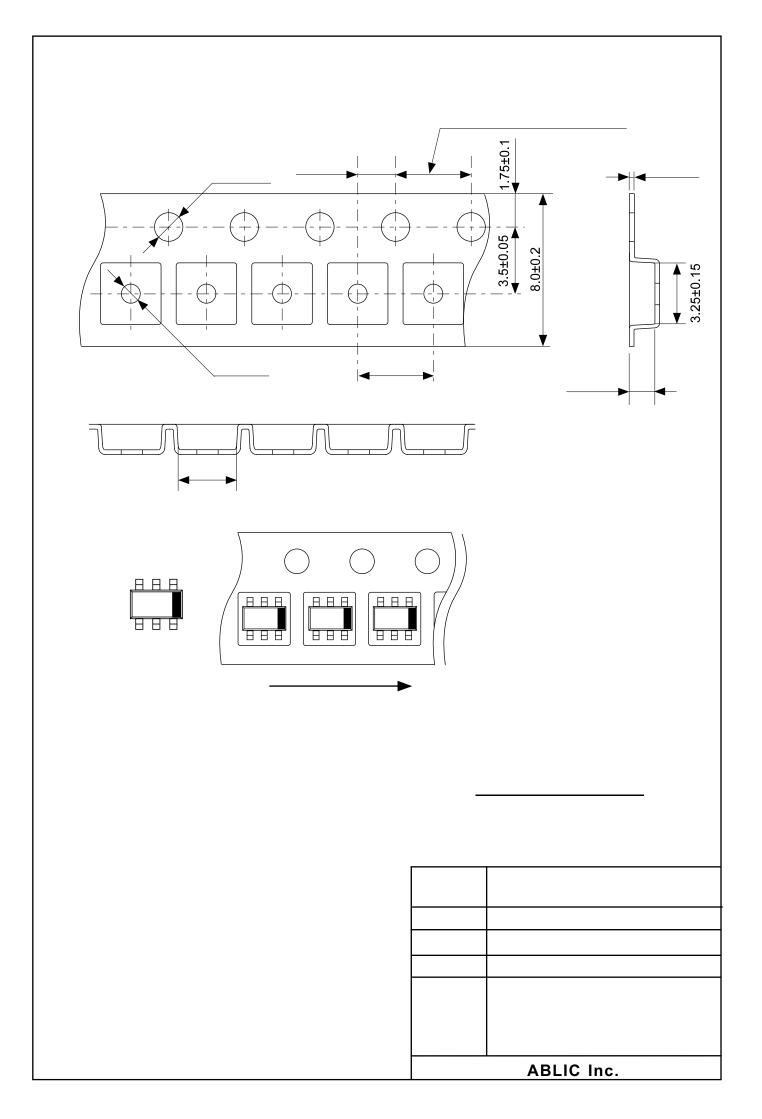
(1) to (3): (4) to (6): Product code (Refer to **Product name vs. Product code**) Lot number

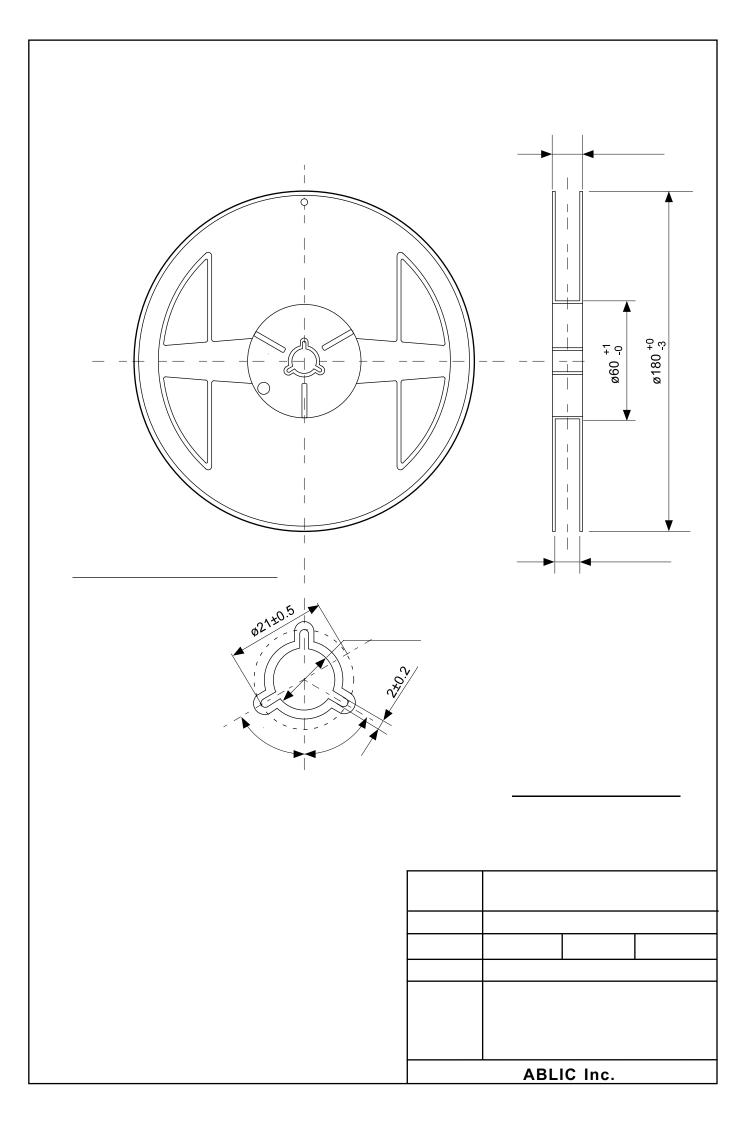
Product name vs. Product code

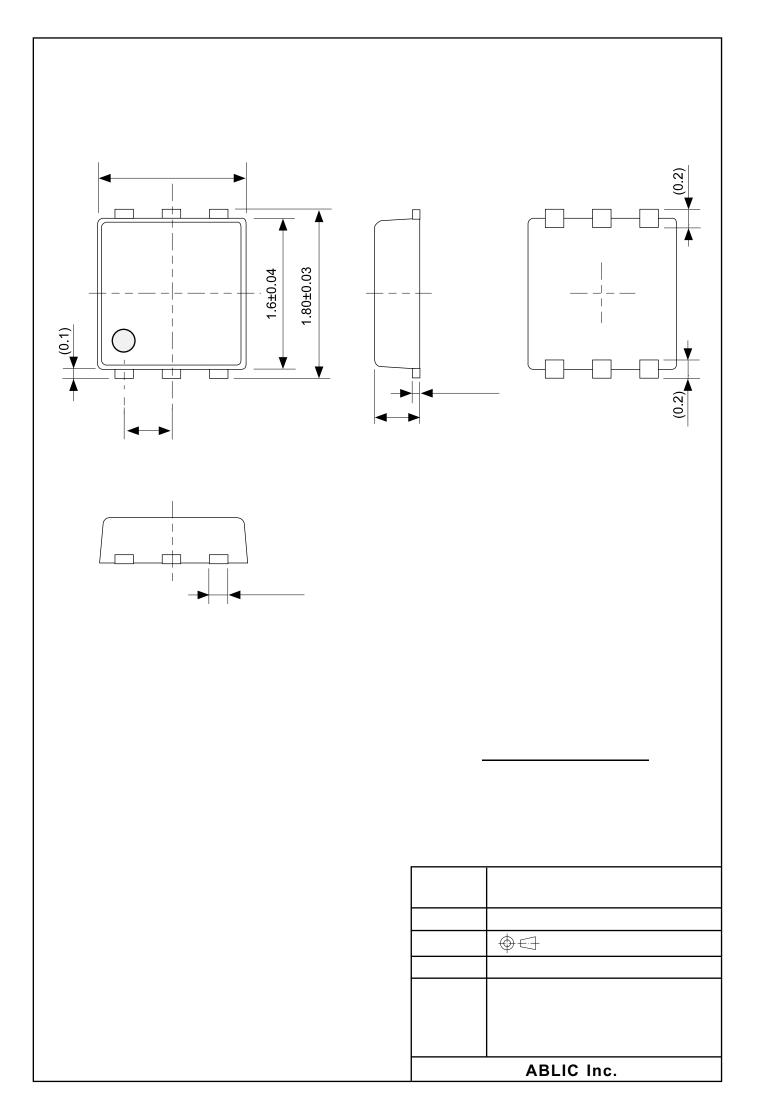
Product Name	Product Code		
	(1)	(2)	(3)
S-5842AAAAL-I6T1U	U	I	L
S-5842AAAAT-I6T1U	U	I	Т
S-5842ABAAA-I6T1U	U	н	А
S-5842ABAAC-I6T1U	U	н	С
S-5842ABAAJ-I6T1U	U	н	J
S-5842ABAAP-I6T1U	U	н	Р
S-5842ABAAV-I6T1U	U	н	V
S-5842ABAAW-I6T1U	U	н	W
S-5842ABAAX-I6T1U	U	н	х
S-5842ACAAS-I6T1U	U	J	S
S-5842ADAAH-I6T1U	U	G	Н
S-5842ADAAK-I6T1U	U	G	К
S-5842ADAAQ-I6T1U	U	G	Q

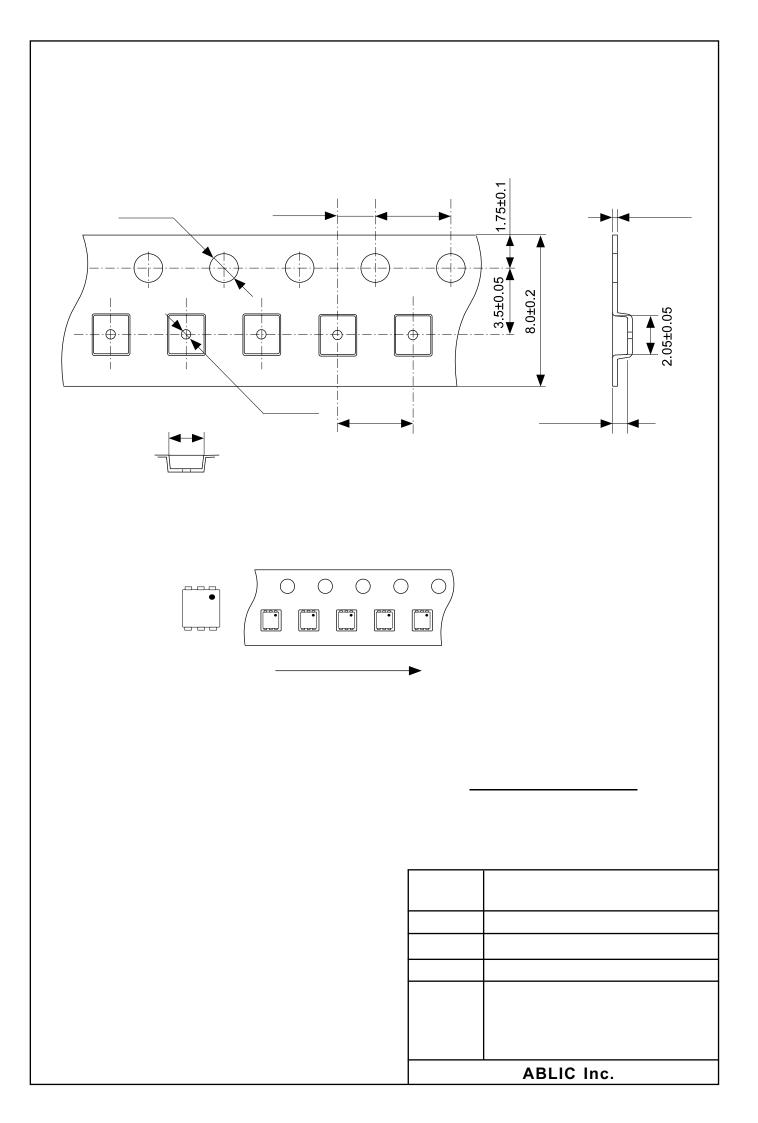


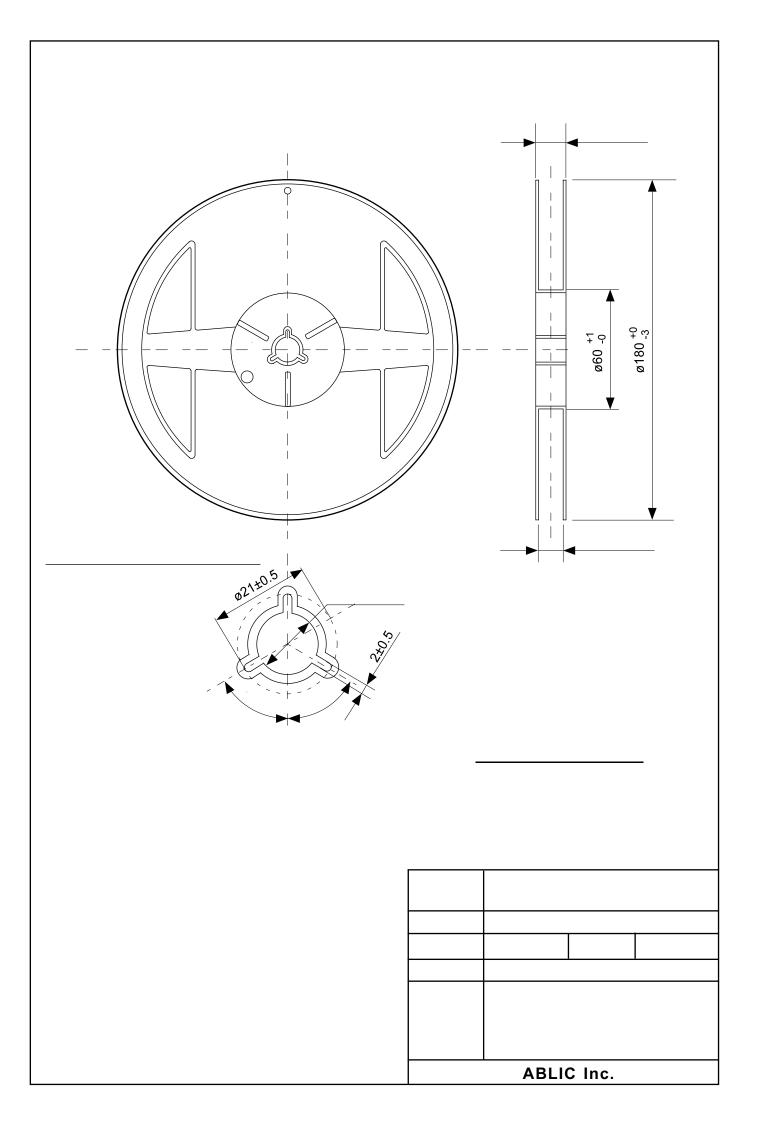


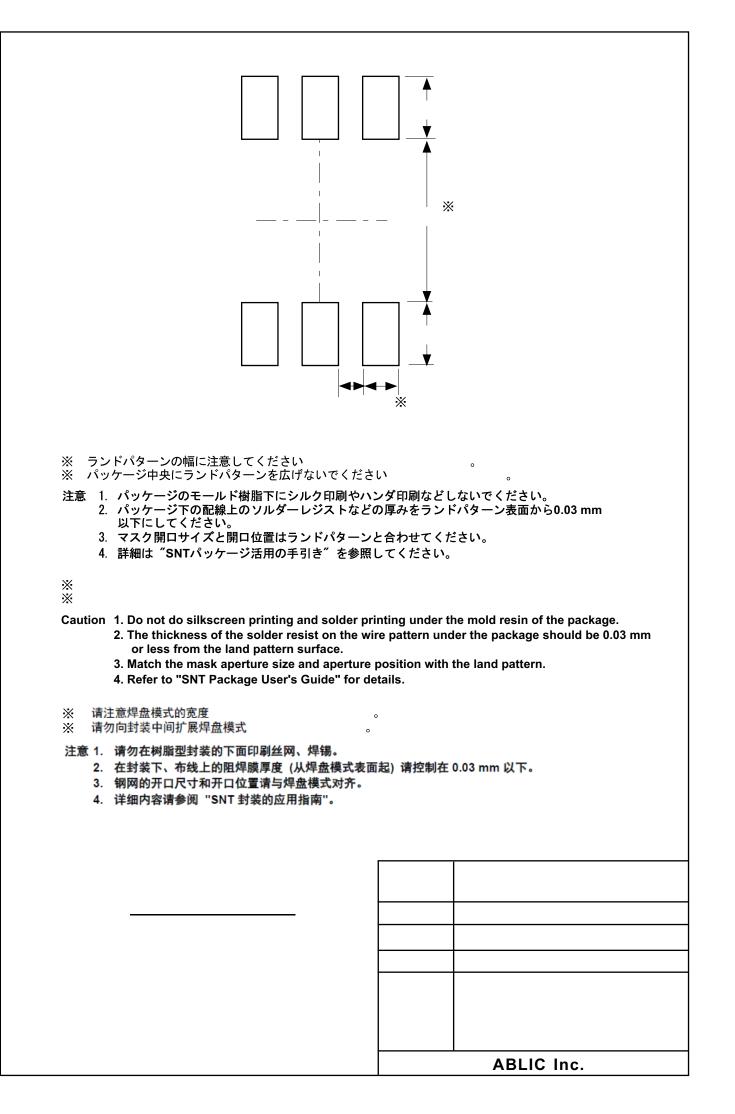












Disclaimers (Handling Precautions)

- 1. All the information described herein (product data, specifications, figures, tables, programs, algorithms and application circuit examples, etc.) is current as of publishing date of this document and is subject to change without notice.
- The circuit examples and the usages described herein are for reference only, and do not guarantee the success of any specific mass-production design.
 ABLIC line is not responsible for damages caused by the reasons other than the products described herein

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 ABLIC Inc. is not responsible for damages caused by failures and / or accidents, etc. that occur due to the use of the products outside their specified ranges.
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- 10. The products are not designed to be radiation-proof. The necessary radiation measures should be taken in the product design by the customer depending on the intended use.
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2.2-2018.06

