

MC-7847-KC

870 MHz GaAs CATV 25 dB POWER DOUBLER AMPLIFIER

FEATURES

- · GaAs ACTIVE DEVICES
- · LOW DISTORTION
- HIGH LINEAR GAIN:
 MC-7847-KC GL = 25 dB MIN at f = 870 MHz
- LOW RETURN LOSS
- · LOW GAIN CHANGE OVER TEMPERATURE
- SPECIFIED FOR 79, 110, and 132 CHANNELS PERFORMANCE
- HIGH RELIABILITY AND RUGGEDNESS:
 Withstands environmental extremes as well as Silicon devices (Surge, ESD, Etc.)

DESCRIPTION

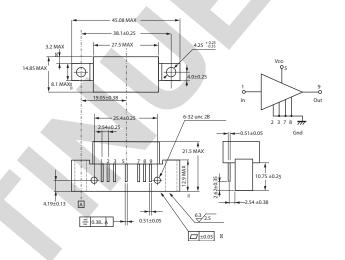
The MC-7847-KC is a GaAs Multi-Chip Modules designed for use as output stages in CATV applications up to 870 MHz. This is a high gain device offering 25 dB minimum gain at 870 MHz. Because this unit is a GaAs device it has low distortion, low noise figure, and low return loss across the entire frequency band.

The MC-7847-KC is similar to the standard push-pull devices, but the higher current allows for better distortion performance, especially X-mod.

Like the previous generation of products, these devices survive such hazards as surge and ESD as well as their silicon competitors, but deliver superior performance with low DC current required. All devices are assembled and tested using fully automated equipment to maximize consistency in part to part performance, and reliability is assured by stringent quality and process control procedures. These parts come in industry compatible hybrid packages.

OUTLINE DIMENSIONS (Units in mm)

PACKAGE OUTLINE H02



APPLICATIONS

- CATV HEADEND SYSTEMS
- · CATV OPTICAL NODES
- CATV DISTRIBUTION AMPS

ELECTRICAL CHARACTERISTICS (TA = 30±5 °C, VDD = 24 V, Zs = ZL = 75 Ω)

PART NUMBER			MC-7847-KC			TEST CONDITIONS
SYMBOLS	CHARACTERISTICS	UNITS	MIN	TYP	MAX	TEST CONDITIONS
BW	Frequency Range	MHz	50	-	870	
GL	Linear Gain	dB	25.0	_	26.0	f = 870 MHz
S	Gain Slope	dB	1.0	1.4	1.8	f = 40 to 870 MHz
Gf	Gain Flatness	dB	-	_	0.6	40 to 870 MHz; Peak to Valley
NF	Noise Figure 1	dB	ı	_	5.5	f = 50 MHz
INF	Noise Figure 2		_	-	6.0	f = 870 MHz
RLi	Input Return Loss	dB	20.0	_	_	40 to 160MHz
			20.0	_	_	160 to 320 MHz
			19.5	_	-	320 to 640 MHz
			17.0	_	_	640 to 870 MHz
RLo	Output Return Loss	mA	20.0	_	_	40 to 160MHz
IDD	Operating Current	mA	350	_	420	RF OFF
CTB	Composite Triple Beat	dBc	_	-	-60	110 Channels
XMod	Cross Modulation ¹	dBc	-	-	-55	Vout = 50 dBmV at 745.25 MHz, 10 dB
CSO	Composite Second Order	dBc	_	_	-63	tilted across the band

^{1.} Measured per US standard methods and procedures (using selective level meter).

ABSOLUTE MAXIMUM RATINGS¹ (TCASE= 30 °C)

SYMBOLS	PARAMETERS	UNITS	RATINGS
VDD	Supply Voltage	V	30
Vı	Input Voltage (Single Tone) ²	dBmV	65
Tc	Operating Case Temperature	°C	-30 to +100
Tstg	Storage Temperature	°C	-40 to +100

Note:

- 1. Operation in excess of any one of these parameters may result in permanent damage.
- 2. Maximum single channel power applied to the input for 1 minute with no measurable degradation in performance.

RECOMMENDED OPERATING CONDITIONS $(Z_s = Z_L = 75\Omega)$

SYMBOLS	PARAMETERS	UNITS	MIN	TYP	MAX
VDD	Supply Voltage V	23.5	24.0	24.5	
Vi	Input Voltage ¹ , MC-7847-KC	dBmV	-	32.0	35.0
Tc	Operating Case Temperature	°C	-30	+25	+85

Note:

1. Test Conditions: 110 Channels, 10 dB tilted across the band.

ORDERING INFORMATION

PART NUMBER	PACKAGE	QUANTITY
MC-7847-KC-AZ	7-pin special with heatsink (Pb-Free)	25 pcs max/ Tray

NOTES ON CORRECT USE

1. The space between PC board and root of the lead should be kept more than 1 mm to prevent undesired stress on the lead and also should be kept less than 4 mm to prevent undesired parasitic inductance.

Recommended space is 2.0 to 3.0 mm typical.

- 2. Recommended torque strength of the screw is 59 to 78 Ncm.
- 3. Form the ground pattern as wide as possible to minimize ground impedance. (to prevent undesired oscillation)

All the ground pins must be connected together with wide ground pattern to decrease impedance difference.

RECOMMENDED SOLDERING CONDITIONS

This product should be soldered in the following recommended conditions. Other soldering methods and conditions than the recommended conditions are to be consulted with our sales representatives.

Soldering	Soldering	Condition
Method	Conditions	Symbol
Pin Part Heating	Pin area temperature: less than 260°C¹ Hour: Within 2 sec./pin	-

Note.

1. The point of pin part heating must be kept at a distance of more than 1.2 mm from the root of lead.

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