

# Dual, 2-Wire Hall-Effect Sensor Interface with Analog and Digital Outputs

### **General Description**

The MAX9621 is a continuation of the Maxim family of Hall-effect sensor interfaces that already includes the MAX9921. The MAX9621 provides a single-chip solution to interface two 2-wire Hall-effect sensors to low-voltage microprocessors ( $\mu$ P) through either a digital output for Hall-effect switches or an analog output for linear information or both.

The MAX9621 protects the Hall sensors from supply transients up to 60V at the BAT supply. Normal operating supply voltage ranges from 5.5V to 18V. If the BAT supply rises above 18V, the MAX9621 shuts off the current to the Hall sensors. When a short-to-ground fault condition is detected, the current to the Hall input is shut off and the condition is indicated at the analog output by a zero-current level and a high digital output.

The MAX9621 provides a minimum of 50µs blanking time following Hall sensor power-up or restart. The opendrain digital outputs are compatible with logic levels up to 5.5V.

The MAX9621 is available in a 3mm x 5mm, 10-pin  $\mu$ MAX<sup>®</sup> package and is rated for operation in the -40°C to +125°C temperature range.

Window Lifters Seat Movers Electric Sunroofs Seatbelt Buckles Door Power Locks Ignition Key Steering Column Speed Sensing

Typical Application Circuit appears at end of data sheet.

### \_Features

- Provides Supply Current and Interfaces to Two 2-Wire Hall-Effect Sensors
- ◆ 5.5V to 18V Operating Voltage Range
- Protects Hall Sensors Against Up to 60V Supply Transients
- Low-Power Shutdown for Power Saving
- Filtered Digital Outputs
- Analog Output Mirrors the Hall Sensor Current
- Hall Inputs Protected from Short to Ground
- Hall Sensor Blanking Following Power-Up and Restart from Shutdown and Short to Ground
- Operates with ±3V Ground Shift Between the Hall Sensor and the MAX9621
- ±2kV Human Body Model ESD and ±200V Machine Model ESD at All Pins
- 3mm x 5mm, 10-Pin µMAX Package

### **\_Ordering Information**

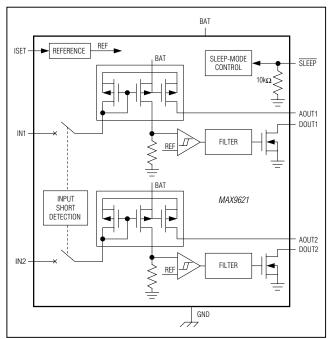
PART	TEMP RANGE	PIN-PACKAGE	
MAX9621AUB+T	-40°C to +125°C	10 µMAX	
MAX9621AUB/V+	-40°C to +125°C	10 µMAX	

+Denotes a lead(Pb)-free/RoHS-compliant package.

T = Tape and reel.

N denotes an automotive qualified part.

### **Functional Diagram**



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For pricing, delivery, and ordering information, please contact Maxim Direct at 1-888-629-4642, or visit Maxim's website at www.maximintegrated.com.

Applications

## **Dual, 2-Wire Hall-Effect Sensor Interface with Analog and Digital Outputs**

### **ABSOLUTE MAXIMUM RATINGS**

BAT to GND	
IN1, IN2 to GND3V to lower of +60V	or (VBAT + 1V)
AOUT1, DOUT1, AOUT2, DOUT2,	
SLEEP to GND	0.3V to +6V
Short-Circuit Duration	
AOUT1, DOUT1, AOUT2, DOUT2 to GND	
or to 5.5V (individually)	Continuous
Current In to IN1, IN2	±100mA
Current In to Any Other Pin	±20mA

Continuous Power Dissipation for a Single-Layer Board ( $T_A = +70^{\circ}C$ )

10-Pin  $\mu$ MAX (derate 5.6mW/°C) above +70°C.......444.4mW Continuous Power Dissipation for a Multilayer Board (TA = +70°C)

()())	
10-Pin µMAX (derate 8.8mW/°C) above +7	′0°C707.3mW
Operating Temperature Range	40°C to +125°C
Junction Temperature	+150°C
Storage Temperature Range	65°C to +160°C
Lead Temperature (soldering, 10s)	+300°C
Soldering Temperature (reflow)	+260°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

### DC ELECTRICAL CHARACTERISTICS

 $(V_{BAT} = 13.6V, V_{SLEEP} = 5V, IN1 = IN2 = no connection, R_{SET} = 61.9k\Omega$  to BAT, R<sub>PU</sub> = 10k $\Omega$  at DOUT1 and DOUT2, R<sub>L</sub> = 5k $\Omega$  to GND at AOUT1 and AOUT2, unless otherwise noted, T<sub>A</sub> = -40°C to +125°C. Typical values are at T<sub>A</sub> = +25°C.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS	
GENERAL							
BAT Supply Range	VBAT	Guaranteed by functional test of $I_{IH},I_{IL},$ and $G_{EI}$	5.5		18	V	
	IBAT	Normal mode			1	mA	
BAT Supply Current	ISD	VSLEEP = 0V		1	10	μA	
		$V_{BAT} = 5.5V$ , at IN1 and IN2, I <sub>IN</sub> = -14mA		0.59	1.26	V	
Hall Input Voltage Dropout	VDO	$V_{BAT} = 5.5V$ , at IN1 and IN2, I <sub>IN</sub> = -20mA		0.86	1.86		
ESD Protection		Machine Model		±200		V	
ESD Frolection		Human Body Model		±2000			
INPUT THRESHOLDS FOR DOU	JT1, DOUT2 S	SWITCHING					
Input Current for Output High	lu i	$R_{SET} = 95.3 k\Omega$			-7.7		
(Note 2)	Ін	$R_{SET} = 52.3 k\Omega$			-14	– mA	
Input Current for Output Low	ΙιL	$R_{SET} = 95.3 k\Omega$	-5			mA	
(Note 2)		$R_{SET} = 52.3k\Omega$	-9				
Input Current Hysteresis for High/Low Detection	lin_hys	Peak-to-peak as percent of average high/ low threshold (Note 2)		8		%	
Channel-to-Channel Input		High threshold		0.02			
Threshold Variation		Low threshold		0.02		mA	
Short-Circuit Current Limit	ISC	A short to GND is not a sustained condition, Hall input reverts to -50µA when detected (Note 2)	-20			mA	
AOUT1, AOUT2 ANALOG OUT	PUTS						
Current Gain for AOUT1 and AOUT2 Outputs	GI	$-18mA \le I_{IN} \le -2mA$		0.05		mA/mA	
Current Gain Error for AOUT1 and AOUT2 Outputs	GEI	I <sub>IN</sub> = -5mA, -14mA		0.2	±1.7	%	

# Dual, 2-Wire Hall-Effect Sensor Interface with Analog and Digital Outputs

### DC ELECTRICAL CHARACTERISTICS (continued)

(VBAT = 13.6V, VSLEEP = 5V, IN1 = IN2 = no connection, RSET = 61.9k $\Omega$  to BAT, RPU = 10k $\Omega$  at DOUT1 and DOUT2, RL = 5k $\Omega$  to GND at AOUT1 and AOUT2, unless otherwise noted, TA = -40°C to +125°C. Typical values are at TA = +25°C.) (Note 1)

PARAMETER	SYMBOL	COND	ITIONS	MIN	TYP	MAX	UNITS
Input Referred Current Offset	los	Inferred from measurements at $I_{IN} = -5mA$ , -14mA		-120		+120	μA
AOUT_ Dropout Voltage		VBAT = 5.5V, for 5% current	I <sub>IN</sub> = -14mA		0.85	1.6	V
		reduction	IIN = -20mA		1.09	1.75	
AOUT_ Output Impedance					500		MΩ
LOGIC I/O (DOUT1, DOUT2)		1					
Output-Voltage Low DOUT1, DOUT2	V <sub>OL</sub>	Sink current = 1mA				0.4	V
Three-State Output Current DOUT1, DOUT2	I <sub>OZ</sub>	$VSLEEP = 0V, 0V \le V$	DOUT_≤5V			±1	μA
SLEEP							
Input-Voltage High	VIH			2.0			V
Input-Voltage Low	VIL					0.8	V
Input Resistance to GND	RIN			50	100		kΩ
AC TIMING CHARACTERISTICS	5			•			
Shutdown Delay from SLEEP Low to IN_ Shutoff	tSHDN	$I_{IH} = -14$ mA to GND, time from SLEEP low to IN_ drop 500mV, CL = 20pF		33	40	46	μs
IN_, Blanking Time at Hall Sensor Power-Up	tBL	$I_{IH} = -14$ mA to GND, time from $V_{IN} = 500$ mV until DOUT_ high, CL = 20pF (Notes 2, 3)		76	89	103	μs
IN_, Current Ramp Rate After Turn-On	tramp	IN_ = GND (Note 2)		3.6	5	6.7	mA/µs
Delay from IN_ to DOUT_ (Filter Delay)	tDEL	From $I_{IH}$ to $I_{IL}$ or from $I_{IL}$ to $I_{IH}$ , $C_L = 20pF$ , Figure 1 (Note 2)		10.8	13.5	16	μs
Delay Difference Between Rising and Falling Edges of Both Channels	tDM	CHALL-BYPASS = $0.01\mu$ F, I <sub>I</sub> H = -11.5mA and I <sub>I</sub> L = -7.5mA, C <sub>L</sub> = 20pF			1		μs
Delay Difference Between Channels	tcc	CHALL-BYPASS = $0.01\mu$ F, $I_{IH}$ = -11.5mA and $I_{IL}$ = -7.5mA, $C_L$ = 20pF			500		ns
Maximum Frequency on Hall Inputs	fMAX	$\label{eq:chall-BYPASS} \begin{array}{l} C_{HALL-BYPASS} = 0.01 \mu \text{F}, \ I_{IH} = -11.5 \text{mA} \\ \text{and} \ I_{IL} = -7.5 \text{mA}, \ C_L = 20 \text{pF} \left( \text{Note } 2 \right) \end{array}$		34	39		kHz
Maximum Analog Output Current During Short-to-GND Fault	Імао				-1.4		mA
IN_ Pulse Length Rejected by Filter to DOUT_	PR	Figure 2 (Note 2)		7.8	11.5	14.6	μs

Note 1: All DC specifications are 100% production tested at  $T_A = +25^{\circ}C$ . AC specifications are guaranteed by design at  $T_A = +25^{\circ}C$ .

Note 2: Parameters that change with the value of the RSET resistor: IIH, IIL, IIN\_HYS, ISC, tBL, tRAMP, tDEL, fMAX, and PR.

Note 3: Following power-up or startup from sleep mode, the start of the blanking period is delayed 20µs.

## **Dual, 2-Wire Hall-Effect Sensor Interface with Analog and Digital Outputs**

### \_Timing Diagrams

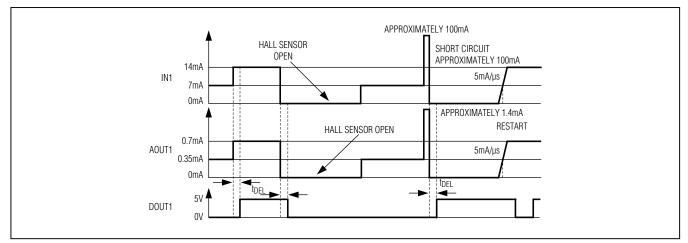


Figure 1. Timing Diagram

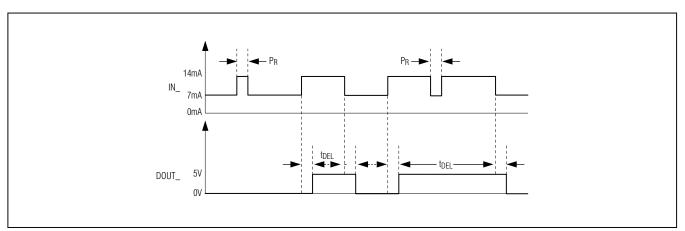
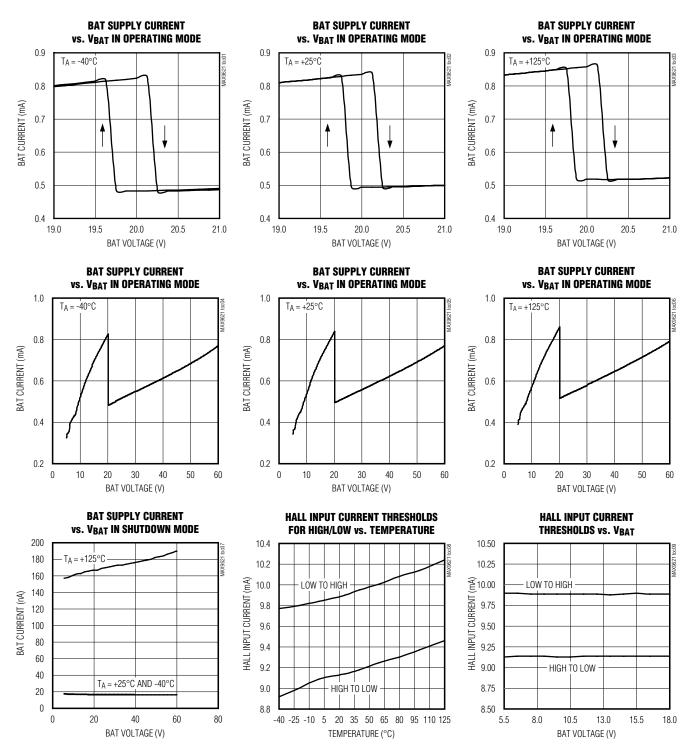


Figure 2. Hall Input Pulse Rejection

# Dual, 2-Wire Hall-Effect Sensor Interface with Analog and Digital Outputs



### Typical Operating Characteristics

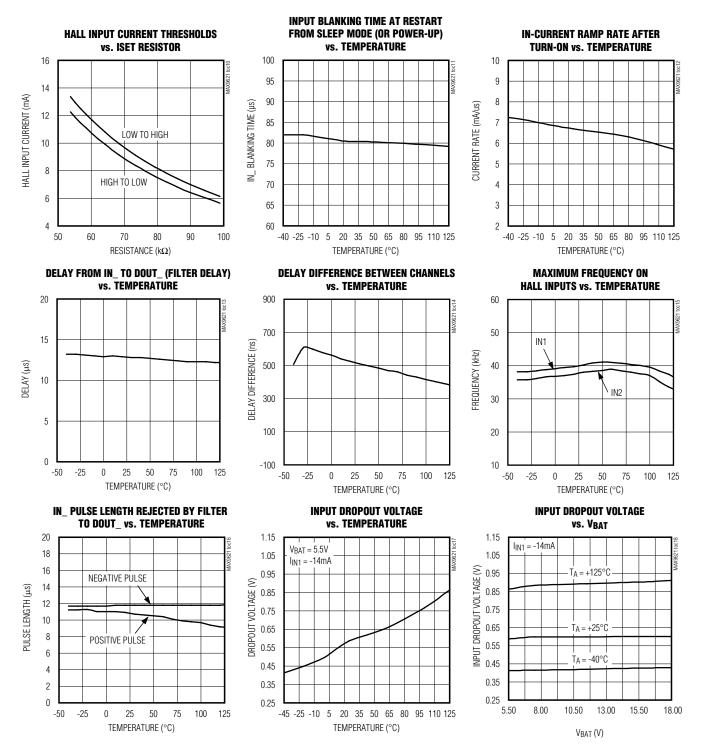
 $(VBAT = 13.6V, RSET = 61.9k\Omega, RL = 5k\Omega$  to GND at AOUT\_, VSLEEP = 5V, TA = +25°C, unless otherwise noted.)

Maxim Integrated

## **Dual, 2-Wire Hall-Effect Sensor Interface with Analog and Digital Outputs**

### Typical Operating Characteristics (continued)

 $(V_{BAT} = 13.6V, R_{SET} = 61.9k\Omega, R_{L} = 5k\Omega$  to GND at AOUT\_,  $V_{SLEEP} = 5V, T_{A} = +25^{\circ}C$ , unless otherwise noted.)

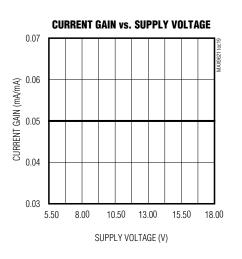


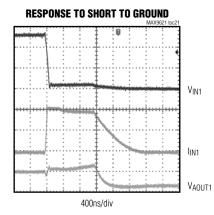
Maxim Integrated

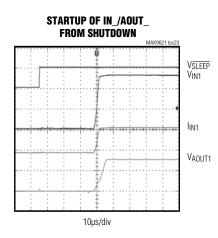
# Dual, 2-Wire Hall-Effect Sensor Interface with Analog and Digital Outputs

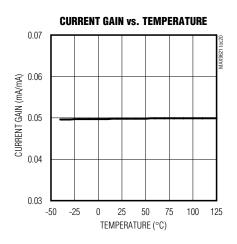
### **Typical Operating Characteristics (continued)**

 $(V_{BAT} = 13.6V, R_{SET} = 61.9k\Omega, R_{L} = 5k\Omega$  to GND at AOUT\_,  $V_{SLEEP} = 5V, T_{A} = +25^{\circ}C$ , unless otherwise noted.)

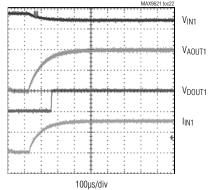




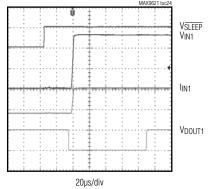




REENERGIZING OF THE HALL INPUT FROM OPEN-CIRCUIT CONDITION

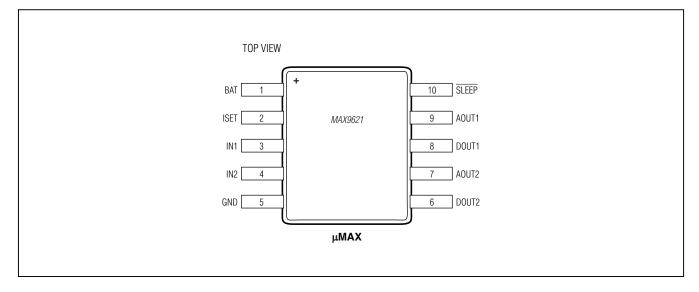


STARTUP OF IN\_/DOUT\_ FROM SHUTDOWN



## **Dual, 2-Wire Hall-Effect Sensor Interface with Analog and Digital Outputs**

Pin Configuration



### Pin Description

PIN	NAME	FUNCTION
1	BAT	Battery Power Supply. Connect to the positive supply through an external reverse-polarity diode. Bypassed to GND with a $0.1\mu$ F capacitor.
2	ISET	Current Setting Input. Place a 1% resistor (R <sub>SET</sub> ) between BAT and ISET to set the desired input current threshold range for the DOUT_ outputs. See the <i>Typical Operating Characteristics</i> section for the correct value of R <sub>SET</sub> for the desired range. Make no other connections to this pin. All routing must have low parasitic capacitance. See the <i>Input Current Thresholds and Short to Ground</i> section.
3	IN1	Hall-Effect Sensor Input 1. Supplies current to the Hall sensor and monitors the current level drawn to determine the high/low state of the sensor. Bypass to GND with a 0.01µF capacitor. Connect an unused input to BAT pin.
4	IN2	Hall-Effect Sensor Input 2. Supplies current to the Hall sensor and monitors the current level drawn to determine the high/low state of the sensor. Bypass to GND with a 0.01µF capacitor. Connect an unused input to BAT pin.
5	GND	Ground

# Dual, 2-Wire Hall-Effect Sensor Interface with Analog and Digital Outputs

### \_Pin Description (continued)

PIN	NAME	FUNCTION
6	DOUT2	Open-Drain Output. Signal translated from Hall sensor 2. DOUT2 is high when the current flowing out of IN2 exceeds the input current threshold high, and is low when less than the input current threshold low. See Table 1 for output response to operating conditions.
7	AOUT2	Analog Current Output. Mirrors the current to the corresponding Hall sensor at IN2. When IN2 has been shut down due to a short to GND a current of zero is supplied to AOUT2. See Table 1 for output response to operating conditions. To obtain a voltage output, connect a resistor from AOUT_ to ground.
8	DOUT1	Open-Drain Output. Signal translated from Hall sensor 1. DOUT1 is high when the current flowing out of IN1 exceeds the input current threshold high, and is low when less than the input current threshold low. See Table 1 for output response to operating conditions.
9	AOUT1	Analog Current Output. Mirrors the current to the corresponding Hall sensor at IN1. When IN1 has been shut down due to a short to GND a current of zero is supplied to AOUT1. See Table 1 for output response to operating conditions. To obtain a voltage output, connect a resistor from AOUT_ to ground.
10	SLEEP	Sleep Mode Input. The part is placed in sleep mode when the $\overline{\text{SLEEP}}$ input is low for more than 40µs. If the $\overline{\text{SLEEP}}$ input is low for less than 20µs and then goes high, the part restarts any Hall input that has been shut off due to a detected short to GND. Any Hall input that is operational is not affected when $\overline{\text{SLEEP}}$ is cycled low for less than 20µs. There is an internal 100k $\Omega$ pulldown resistance to GND.

### **Detailed Description**

The MAX9621, an interface between two 2-wire Halleffect sensors and a low-voltage microprocessor, supplies and monitors current through IN1 and IN2 to two Hall sensors.

The MAX9621 complements Maxim's existing family of Hall-effect sensor interfaces that includes the MAX9921.

The MAX9621 provides two independent channels with two outputs for each channel, a digital output, and an analog output. The digital outputs (DOUT1 and DOUT2) are open-drain and indicate a logic level that corresponds to the Hall sensor status. DOUT1 or DOUT2 outputs high when the current out of IN1 or IN2, respectively, exceeds the high-input current threshold. DOUT1 or DOUT2 outputs low when the current flowing out of IN1 or IN2, respectively, is lower than the low-input current threshold. DOUT1 and DOUT2 provide a time domain output filter for robust noise immunity. See Figure 2.

The analog outputs (AOUT1 and AOUT2) mirror the current flowing out to the corresponding inputs IN1 and IN2 with a nominal gain of 0.05mA/mA.

#### Hall Sensor Protection from Supply Transients

The MAX9621 protects the hall sensors from supply transients by shutting off current at IN1 and IN2 when the BAT voltage is 18V. The digital outputs go low and analog outputs have zero output current. When VBAT returns to the proper operating range, both inputs restart following a blanking cycle.

## **Dual, 2-Wire Hall-Effect Sensor Interface with Analog and Digital Outputs**

CONDITION	AOUT_	DOUT_
IN_ Short to GND	0	High-Z
IN_ Short to BAT or IN_ Open	0	Low*
SLEEP Low	0	High-Z
V <sub>BAT</sub> > 18V	0	Low*

#### Table 1. AOUT\_/DOUT\_ Truth Table

\*If IN\_ is already shorted to BAT or open during power-up, DOUT\_ goes to high-Z until IN\_ is loaded.

#### Hall Input Short-to-Battery Condition

The MAX9621 interprets a short to battery when the voltage at IN1 or IN2 is higher than  $V_{BAT}$  - 100mV. The digital outputs go low and the analog outputs are set to zero output current. If IN1 or IN2 is more than 1V above VBAT, it back-drives current into BAT. The MAX9621 restarts the Hall inputs when the Hall input is loaded again.

#### Hall Input Short to Ground

The Hall input short-to-ground fault is effectively a latched condition if the input remains loaded by the Hall switch. The current required to power the Hall switch is shut off and only a 50µA pullup current remains. The Hall input can be manually reenergized or it can be reenergized by the µP. A 10µs to 20µs negative pulse at SLEEP restarts with a blanking cycle any Hall input that has been shut down due to the short-to-ground condition. During startup or restart, it is possible for a Hall input to charge up an external capacitance of 0.02µF without

tripping into a short-to-ground latched state. During the short-to-ground fault, DOUT1 and DOUT2 are high impedance (pulled high by the pullup resistors), while AOUT1 and AOUT2 are set to zero-output current.

#### Manual Method for Reenergizing Hall Sensor and Means for Diagnosing an Intermittent Hall Sensor Connection

Figure 3 shows the behavior of the MAX9621 when a Hall input is open. Figure 4 shows the behavior of the MAX9621 when the open input is reconnected to a Hall sensor. Figures 3 and 4 demonstrate how a short-to-ground Hall input can be reset. Resetting a short-to-ground Hall input involves three steps:

- 1) Relieve the short to ground at the Hall sensor.
- 2) Disconnect the Hall input from the Hall sensor (openinput fault condition).
- 3) Reconnect the Hall input to the Hall sensor.

The MAX9621 restarts the Hall input with a blanking cycle. If the Hall input is disconnected from the Hall sensor for 10ms, it allows the Hall input to be pulled up by the 50 $\mu$ A pullup current to register the open-input fault condition. Reconnecting the Hall input to the Hall sensor restarts the Hall input with a blanking cycle. This provides a manual means of reenergizing a Hall input without having to resort to the  $\mu$ P to restart it. This also demonstrates the behavior of an intermittent connection to a Hall sensor.

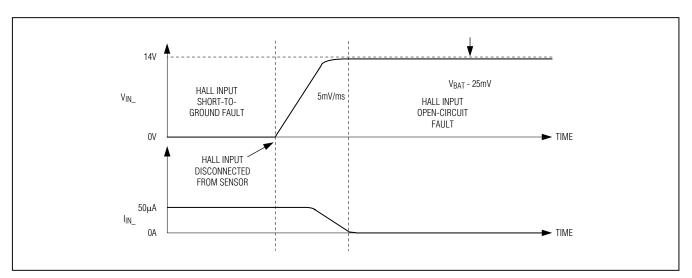


Figure 3. Hall Input Ramps to Open-Circuit Fault When a Short to Ground Is Relieved

## Dual, 2-Wire Hall-Effect Sensor Interface with Analog and Digital Outputs

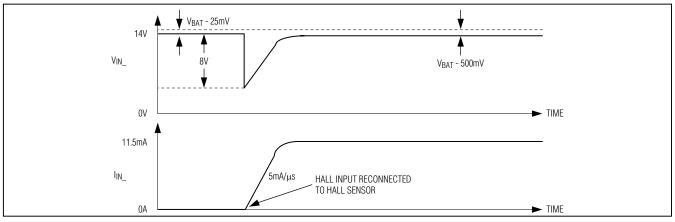


Figure 4. Hall Input Reenergized When Open Input Is Reconnected to Hall Sensor

#### Sleep Mode Input (SLEEP)

The MAX9621 features an active-low SLEEP input. Pull SLEEP low for more than 40µs to put the device into sleep mode for power saving. In sleep mode, the DOUT1 and DOUT2 outputs are high impedance and are pulled high by pullup resistors. AOUT1 and AOUT2 are set to zero-output current.

#### Hall Input Restart

When an input has been shut down due to a short to ground, cycle  $\overline{\text{SLEEP}}$  for 10µs to 20µs to restart the input. If the other input is operational it is not affected. The restart happens on the rising edge of  $\overline{\text{SLEEP}}$ .

#### Input Current Thresholds and Short to Ground

The input current high and low thresholds that determine the logic level of the digital outputs are adjusted by changing the RSET value. When the RSET value changes, the following parameters change as well: IIN\_HYS, ISC, tBL, tRAMP, tDEL, fMAX, and PR.

IIH, IIL, IIN\_HYS, ISC, tRAMP, and fMAX are inversely proportional to RSET and decrease as RSET increases. This inverse relationship is linear. For example, a 10% change in (1/RSET) results in a 10% change in current parameters. Conversely, time and delay parameters are linear and directly proportional to RSET, and a 10% change in RSET results in an 10% change in time parameters.

The difference between the maximum and minimum threshold current limits is the min/max limit spread, which is greater than the threshold hysteresis. The min/max spread and the hysteresis both change by the same percentage as the mean of the threshold current limits. The following equation is useful for finding the mean of the threshold current limits given a value of RSET resistance:

$$I = I_0 + \frac{1}{R \times m} \left( I < 0 \right)$$

I is the mean of the threshold current limits, R is the value of the R<sub>SET</sub> resistance in k $\Omega$ , the constant I<sub>0</sub> = 0.03717mA, and the constant m = -0.001668 (1/(k $\Omega \times mA$ )).

The following equation is useful for finding the value of RSET resistance given a mean of the threshold current limits:

$$Y = Y_0 + m \times I (I < 0)$$
$$R = \frac{1}{Y}$$

$$f_0 = 6.2013 \text{ x } 10^{-5} \text{ units of } (1/k\Omega)$$

To compute the typical input current thresholds from the mean input current, it is necessary to obtain the hysteresis. The following equation finds the hysteresis given the mean threshold current, I:

$$H = H_0 + k \times I (I < 0)$$

where  $H_0 = -0.033463$  in mA, and k = -0.08414 in mA/mA. Input current threshold high = I - H/2, input current threshold low = I + H/2.

### Application Information

#### **Use of Digital and Analog Outputs**

The digital output can be used to provide the  $\mu$ P with an interrupt signal that can represent a Hall sensor change of status. DOUT1 and DOUT2 provide a time domain output filter for robust noise immunity. See Figure 2. The analog output can be connected to an ADC with an appropriate load resistor, and can be used to perform custom diagnostics.

## **Dual, 2-Wire Hall-Effect Sensor Interface with Analog and Digital Outputs**

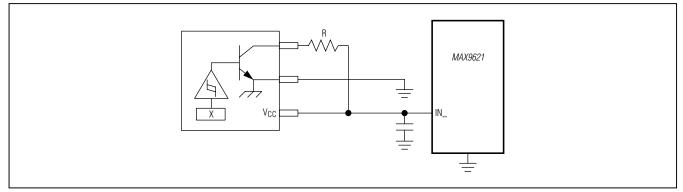


Figure 5. 3-Wire Hall-Effect Switches Configured as 2-Wire

### Table 2. A Partial List of Compatible Hall Switches

PART NO.	MANUFACTURER	WEBSITE	COMMENTS
HAL573-6	Micronas	www.micronas.com	2-wire
HAL556/560/566	Micronas	www.micronas.com	2-wire
HAL579/581/584	Micronas	www.micronas.com	2-wire
A1140/1/2/3	Allegro	www.allegromicro.com	2-wire
A3161	Allegro	www.allegromicro.com	3-wire, optimized for 2-wire use without added resistor
TLE4941/C	Infineon	www.infineon.com	2-wire

#### Sleep Mode

Sleep mode can be used in applications that do not continuously require the polling of the Hall sensors. In such cases, the  $\mu$ P can enable the MAX9621 for a short time, check the sensor status, and then put the MAX9621 back to sleep. A blanking period follows upon exiting sleep mode.

#### **Remote Ground**

The MAX9621 targets applications with 2-wire Hall-effect sensors. 2-wire sensors have connections for supply and ground. The output level is signaled by means of modulation of the current drawn by the Hall sensor from its supply. The two threshold currents for high/low are generally in the range of 5mA to 14mA. Thus, the interfacing of a 2-wire sensor is not simply a matter of detecting two voltage thresholds, but requires a coarse current-sense function.

Because of the high-side current-sense structure of the MAX9621, the device is immune to shifts between the

sensor ground, the ground of the MAX9621 and  $\mu P.$  This ground-shift immunity eliminates the need for a ground-connection wire, allowing a single-wire interface to the Hall sensor.

#### **Hall-Effect Sensor Selection**

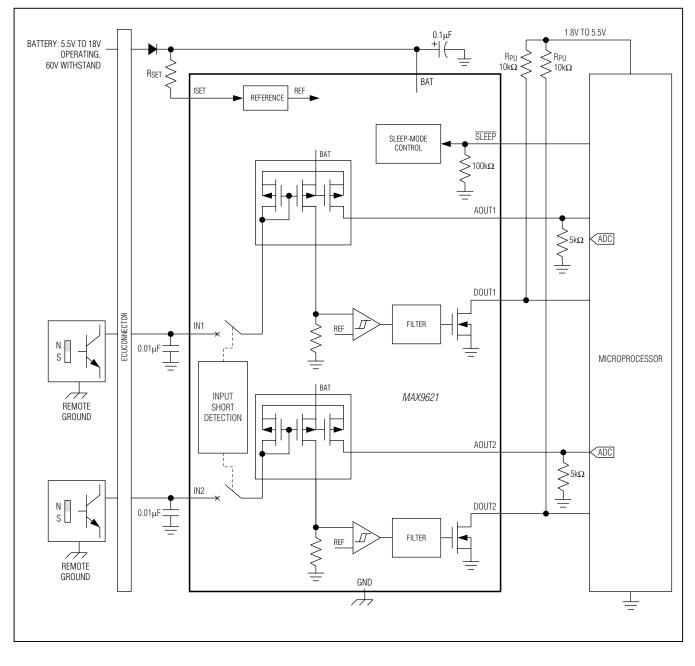
The MAX9621 is optimized for use with 2-wire Hall-effect switches or with 3-wire Hall-effect switches connected as 2-wire (Figure 5). When using a 3-wire Hall sensor the resistor R is chosen so that the current drawn by the Hall sensor crosses the MAX9621 current threshold when the magnetic threshold of the Hall sensor is exceeded. A partial list of Hall switches that can be used with the MAX9621 is given in Table 2.

#### **Input Current Threshold Precision**

To get the best input current threshold precision, it is recommended that the R<sub>SET</sub> resistor be directly connected to the BAT pin. A true Kelvin type connection is best.

# Dual, 2-Wire Hall-Effect Sensor Interface with Analog and Digital Outputs

### **Typical Application Circuit**



### \_Chip Information

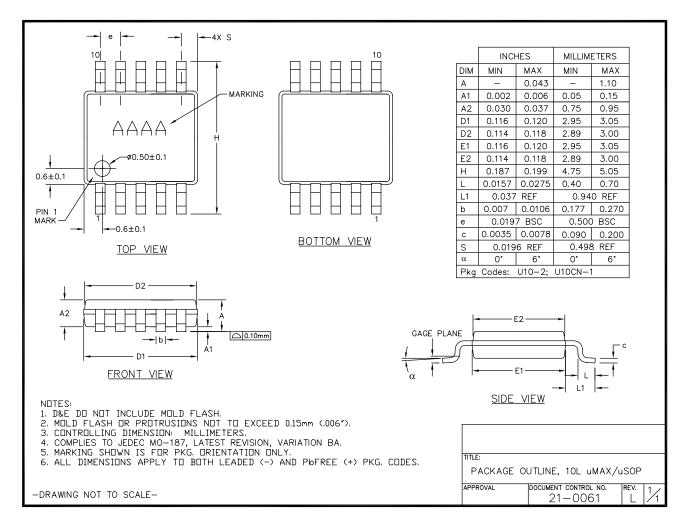
PROCESS: BICMOS

# **Dual, 2-Wire Hall-Effect Sensor Interface with Analog and Digital Outputs**

### Package Information

For the latest package outline information and land patterns (footprints), go to <u>www.maximintegrated.com/packages</u>. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

PACKAGE TYPE	PACKAGE CODE	OUTLINE NO.	LAND PATTERN NO.
10 µMAX	U10+2	<u>21-0061</u>	<u>90-0330</u>



## Dual, 2-Wire Hall-Effect Sensor Interface with Analog and Digital Outputs

### **Revision History**

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	11/09	Initial release	—
1	9/11	Added automotive qualified part	1



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