## Precision Sub-Miniature 5.0x3.2mm TCXO / VCTCXO Designed for Telecom Applications



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## **Description:**

The Connor-Winfield 5.0x3.2mm Temperature Compensated Crystal Oscillators and Voltage Controlled Temperature Compensated Crystal Oscillators are designed

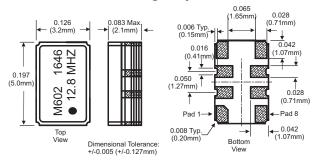


for use in applications requiring tight frequency stability in a small package. Through the use of Analog Temperature Compensation, this device is capable of holding sub 1-ppm stabilities over wide temperature ranges.

#### Features:

- 3.3V Operation
- LVCMOS or clipped Sinewave Output Logic
- Sub-Miniature 5.0x3.2mm SMT Package
- Frequency Stabilities Available: ✓stratum 3 ±0.28 ppm with Stratum 3 Holdover ±0.50 ppm or ±1.00 ppm
- Temperature Ranges Available: 0 to 70°C; 0 to 85°C; -20 to 70°C; -40 to 85°C
- Low Power <10mA</li>
- Low Jitter <1pS RMS
- Low Phase Noise
- Tape and Reel Packaging
- RoHS Compliant / Lead Free 
   √RoHS
- Recommended for new designs

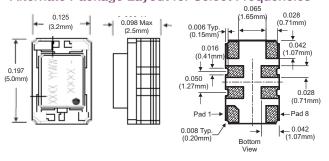
#### Package Layout



## **Applications:**

- STRATUM 3 Applications
- GPS Receivers
- Instrumentation
- Femtocells
- FTTH, FTTC

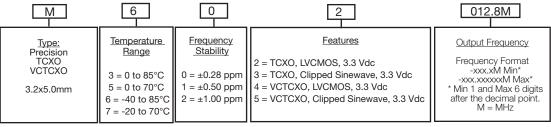
#### Alternate Package Layout for Select Frequencies



## **Pad Connections**

Pau	Connection
1:	Volltage Control or N/C
2:	Do Not Connect
3:	Do Not Connect
4:	Ground
5:	Output
6:	Do Not Connect
7:	Do Not Connect
8:	Supply, Vcc

## Ordering Information



See page 3 for frequency range information on each part number.

#### Example:

 $M602\text{-}012.8M = 3.2x5\text{mm, TCXO, LVCMOS,} \\ 3.3\text{Vdc, -}40^{\circ} \text{ to } 85^{\circ}\text{C, } \pm 0.28\text{ppm, Output Frequency } 12.8\text{MHz} \\ \text{To order an M602 with an output frequency of: } 6.4\text{MHz} = \text{M602-006.4M} \\ \text{Consult the factory for available frequencies} \\ \\$ 



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	Absolute Maxi	mum Ratings			
Parameter	Minimum	Nominal	Maximum	Units	Notes
Storage Temperature	-55	-	85	°C	
Supply Voltage (Vcc)	-0.5	-	6.0	Vdc	
Input Voltage (Vc)	-0.5	-	Vcc + 0.5	Vdc	
	Operating Sp	ecifications			
Parameter	Minimum	Nominal	Maximum	Units	Notes
TCXO Frequency Calibration @ 25°C	-1.0	-	1.0	ppm	1
Supply Voltage Variation. (Vcc±5%)	-0.2	-	0.2	ppm	
Load Coefficient, ±5%	-0.2	-	0.2	ppm	
Static Temperature Hysteresis	-0.4	-	0.4	ppm	2
Aging First Year	-1.0	-	1.0	ppm	
Total Frequency Tolerance (20 Years)	-4.6	-	4.6	ppm	3
Supply Voltage (Vcc)	3.135	3.3	3.465	Vdc	4
Supply Current (Icc)	-	6	10	mA	
Period Jitter	-	3	5	ps rms	
Integrated Phase Jitter (BW=12kHz to 20MHz)	-	0.3	1.0	ps rms	
SSB Phase Noise at 10Hz offset	-	-90	-70	dBc/Hz	
SSB Phase Noise at 100Hz offset	_	-115	-100	dBc/Hz	
SSB Phase Noise at 1 KHz offset	-	-135	-130	dBc/Hz	
SSB Phase Noise at >10KHz offset	_	-152	-145	dBc/Hz	
SSB Phase Noise at >100KHz offset	_	-154	-150	dBc/Hz	
Start Up Time		-	10	ms	
	Characteristics for	Voltage Contro			
Parameter	Minimum	Nominal	Maximum	Units	Notes
Control Voltage Range (Vcc = 3.3V) (Vc)	0.3	1.65	3.0	Vdc	
Frequency Tuning measured @ 25°C	±10	-	-	ppm	5
Linearity	±5	-	-	%	
Slope	Positive				
Input Impedance	100K	-	-	Ohms	
Modulation Bandwidth (3dB)	10	-	-	KHz	
,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	LVCMOS Output	Characteristics	<u> </u>		
Parameter	Minimum	Nominal	Maximum	Units	Notes
LOAD	-	15	-	pF	6
Voltage (High) (Voh)	90% Vcc	-		Vdc	0
(Low) (Vol)	-	-	10% Vcc	Vdc	
Current (High) (loh)			-4	mA	
(Low) (IoI)	4	_	' -	mA	
Duty Cycle at 50% of Vcc	45	50	55	%	
Rise / Fall Time 10% to 90%	-	-	8	ns	
	ipped Sinewave Ou	itnut Character	-	110	
Parameter	Minimum	Nominal	Maximum	Units	Notes
LOAD	-	INOITIIIIai	-	Office	7
Output Load Resistance		 10K		Ohms	6
Output Load nesistance Output Load Capacitance	<u> </u>	10	-	pF	6
Output Load Capacitance Output Voltage (< 40 MHz)	1.0	1.2		ν	pk-pk
Output Voltage (< 40 MHz)	0.8		-	V V	
Output Voltage (=>40 MHz) Output Impedance	U.O	1.0	-		pk-pk
output impedance	<del>-</del>	200	-	Ohms	

#### Notes:

- 1) TCXO: Initial calibration @ 25°C. Specifications at time of shipment after 48 hours of operation.
- 2) Frequency change after reciprocal temperature ramped over the operating range. Frequency measured before and after at 25°C.
- 2) Inclusive of calibration @ 25°C, frequency vs. change in temperature, change in supply voltage (±5%), load change (±5%), reflow soldering process and 20 years aging.

  4) For best in application performance, careful selection of an external power source is critical. Select an external regulator that meets or exceeds to the following
- specifications regarding voltage regulation tolerance, initial accuracy, temperature coefficient, voltage noise, and low voltage noise density. Factory Test Conditions: Initial Accuracy ±2mv, Noise (0.1Hz to 10KHz) 15UV p-p, Voltage Noise Density = 50nV/ (Square root Hz), Temperature Coefficient <5ppm°C.

  5) Additional pull ranges are available; please contact the factory for additional information.
- 6) Attention: To achieve optimal frequency stability, and in some cases to meet the specification stated on this datasheet, it is required that the circuit connected to this TCXO output must have the equivalent input capacitance that is specified by the nominal load capacitance. Deviations from the nominal load capacitance will have a graduated effect on the stability of approximately 20ppb per pF load difference.
- 7) Output is DC coupled.

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## **Model Specifications**

Model Number	M502	M503	M504	M505	Notes	
Output Type	LVCMOS	Clipped Sinewave	LVCMOS	Clipped Sineway	/e	
TCXO/VCTCXO	TCXO	TCX0	VCTCXO	VCTCXO		
Frequency Range	6.4 to 49.152 MHz					
Frequency Stability		±0.28p	pm		1	
Supply Voltage		3.3Vc	dc			
Temperature Range		0 to 70	)°C			
Holdover Stability		±0.32p	pm		2	

Model Number	M302	M303	M304	M305	Notes	
Output Type	LVCMOS	Clipped Sinewave	LVCMOS	Clipped Sineway	ve	
TCXO/VCTCXO	TCXO	TCX0	VCTCXO	VCTCXO		
Frequency Range		6.4 to 49.152 MHz				
Frequency Stability		±0.28ppm				
Supply Voltage		3.3Vd	С			
Temperature Range		0 to 85	°C			
Holdover Stability		±0.32p	pm		2	

Model Number	M512	M513	M514	M515	Notes
Output Type	LVCMOS	Clipped Sinewave	LVCMOS	Clipped Sinewa	ave
TCXO/VCTCXO	TCXO	TCX0	VCTCXO	VCTCXO	
Frequency Range		6.4 to 49.	152 MHz		
Frequency Stability		±0.50p	pm		1
Supply Voltage		3.3٧0	dc		
Temperature Range	0 to 70°C				
Model Number	M312	M313	M314	M315	Notes
	M312 LVCMOS	M313 Clipped Sinewave	M314 LVCMOS	M315 Clipped Sinewa	
Model Number					
Model Number Output Type	LVCMOS	Clipped Sinewave	LVCMOS VCTCXO	Clipped Sinewa	
Model Number Output Type TCXO/VCTCXO	LVCMOS	Clipped Sinewave TCXO	LVCMOS VCTCXO 152 MHz	Clipped Sinewa	
Model Number Output Type TCXO/VCTCXO Frequency Range	LVCMOS	Clipped Sinewave TCXO 6.4 to 49.	LVCMOS VCTCXO 152 MHz	Clipped Sinewa	

Model Number	M522	M523	M524	M525	Notes	
Output Type	LVCMOS	Clipped Sinewave	LVCMOS	Clipped Sinewa	ive	
TCXO/VCTCXO	TCXO	TCX0	VCTCXO	VCTCXO		
Frequency Range	6.4 to 52 MHz					
Frequency Stability		±1.00p	pm		1	
Supply Voltage		3.3Va	dc			
Temperature Range		0 to 70	)°C			
Model Number	M322	M323	M324	M325	Notes	
Model Number Output Type	M322 LVCMOS	M323 Clipped Sinewave	M324 LVCMOS	M325 Clipped Sinewa		
		*****				
Output Type	LVCMOS	Clipped Sinewave	LVCMOS VCTCXO	Clipped Sinewa		
Output Type TCXO/VCTCXO	LVCMOS	Clipped Sinewave TCXO	LVCMOS VCTCXO MHz	Clipped Sinewa		
Output Type TCXO/VCTCXO Frequency Range	LVCMOS	Clipped Sinewave TCXO 6.4 to 52	LVCMOS VCTCXO MHz	Clipped Sinewa		

## **Model Specifications**

Model Number	M702	M703	M704	M705	Notes
Output Type	LVCMOS	Clipped Sinewave	LVCMOS	Clipped Sineway	e
TCXO/VCTCXO	TCXO	TCX0	VCTCXO	VCTCXO	
Frequency Range		6.4 to 49.	152 MHz		
Frequency Stability	±0.28ppm				
Supply Voltage	3.3Vdc				
Temperature Range		-20 to 7	0°C		
Holdover Stability		±0.32p	pm		2

Model Number	M602	M603	M604	M605	Notes
Output Type	LVCMOS	Clipped Sinewave	LVCMOS	Clipped Sineway	e
TCXO/VCTCXO	TCXO	TCX0	VCTCXO	VCTCXO	
Frequency Range	6.4 to 49.152	6.4 to 49.152	6.4 to 49.152	6.4 to 49.152	MHz
Frequency Stability	±0.28ppm				
Supply Voltage		3.3	3Vdc		
Temperature Range		-40 to	o 85°C		
Holdover Stability		±0.3	32ppm		2

Model Number	M712	M713	M714	M715	Notes
Output Type	LVCMOS	Clipped Sinewave	LVCMOS	Clipped Sineway	е
TCXO/VCTCXO	TCXO	TCX0	VCTCXO	VCTCXO	
Frequency Range		6.4 to 49	.152 MHz		
Frequency Stability		±0.50	opm		1
Supply Voltage		3.3V	dc		
Temperature Range		-20 to 7	70°C		
Model Number	M612	M613	M614	M615	Notes
Output Type	LVCMOS	Clipped Sinewave	LVCMOS	Clipped Sineway	е
TCX0/VCTCX0	TCXO	TCX0	VCTCXO	VCTCXO	
Frequency Range	6.4 to 60	6.4 to 49.152	6.4 to 49.15	2 6.4 to 49.152	MHz
Frequency Stability		±0.5	0ppm		1
Supply Voltage		3.3	Vdc		
Temperature Range		10:	85°C		

Model Number	M722	M723	M724	M725	Notes
Output Type	LVCMOS	Clipped Sinewave	LVCMOS	Clipped Sinewa	ve
TCXO/VCTCXO	TCXO	TCX0	VCTCXO	VCTCXO	
Frequency Range		6.4 to 52	MHz		
Frequency Stability		±1.00p	pm		1
Supply Voltage		3.3Vd	С		
Temperature Range		-20 to 7	0°C		
Model Number	M622	M623	M624	M625	Notes
Model Number Output Type	M622 LVCMOS	M623 Clipped Sinewave	M624 LVCMOS	M625 Clipped Sinewa	
l		*****			
Output Type	LVCMOS	Clipped Sinewave	LVCMOS VCTCXO	Clipped Sinewa	
Output Type TCXO/VCTCXO	LVCMOS	Clipped Sinewave TCXO	LVCMOS VCTCXO MHz	Clipped Sinewa	
Output Type TCXO/VCTCXO Frequency Range	LVCMOS	Clipped Sinewave TCXO 6.4 to 52	LVCMOS VCTCXO MHz Oppm	Clipped Sinewa	

#### Notes

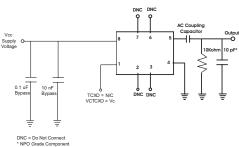
1) Frequency stability vs. change in temperature. [±(Fmax - Fmin)/2.Fo].

**LVCMOS Test Circuit** 

2) Inclusive of frequency stability, supply voltage change (±1%), aging, for 24 hours.

# Voc Supply Ovoltage 0.1 uF Bypass Bypass TCXD = NiC DNC DNC VCTCXD = VC VCTCXD = VC VCTCXD = VC

## Clipped Sinewave Test Circuit



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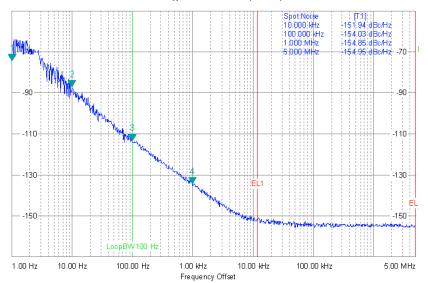
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#### **Environmental Characteristics**

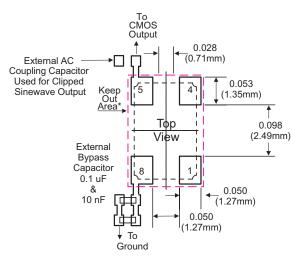
Vibration:	Vibration per Mil Std 883E Method 2007.3 Test Condition A
Shock:	Mechanical Shock per Mil Std 883E Method 2002.4 Test Condition B.
Soldering:	RoHS compliant lead free. See soldering profile below.
Solderability	Solderability per Mil Std 883F Method 2003

# Typical Phase Noise

M602-020.0M Typical Phase Noise (dBc/Hz)

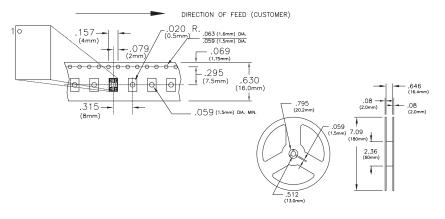


## Suggested Pad Layout

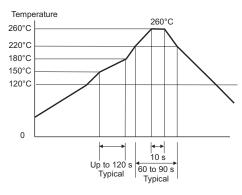


\* Do not route any traces in the keep out area. It is recommended that the next layer under the keep out area is to be ground plane.

## **Tape and Reel Specifications**

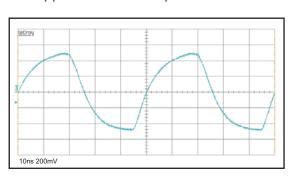


### Solder Profile

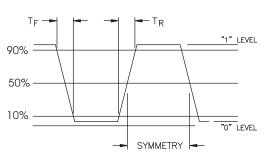


Meets IPC/JEDEC J-STD-020C

## **Clipped Sinewave Output Waveform**



## **LVCMOS Output Waveform**



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