

Small-sized Class-D Speaker Amplifiers

# Analog Input

# Monaural Class-D Speaker Amplifier



BD5461GUL

No.10101EAT02

## h Description

BD5461GUL is a low voltage drive class-D monaural speaker amplifier that was developed for cellular phones, mobile audio products and the others. LC-filter of speaker output is unnecessary, and the number of external components is three. It is suitable for the application of battery drive because of high efficiency and low power consumption. Also, stand-by current is 0μA (typ.), and fast transitions from standby to active with little pop noise. It is suitable for applications that switch repeatedly between stand-by and active.

## h Features

- 1) No LC filter required
- 2) Only three external components
- 3) High power 2.5W/4 /BTL (VDD=5V, RL=4 , THD+N=10%, typ.)
- 4) High power 0.85W/8 /BTL (VDD=3.6V, RL=8 , THD+N=10%, typ.)
- 5) Gain 12dB
- 6) Analog differential input / PWM digital output
- 7) Pop noise suppression circuitry
- 8) Built-in standby function
- 9) Protection circuitry (Short protection [Auto recover without power cycling], Thermal shutdown, Under voltage lockout)
- 10) Very small package 9-Bump WL-CSP (1.6\*1.6\*0.55mmMAX)

## h Applications

Mobile phones, PDA, Mobile electronics applications, Note-book PC etc.

## h Absolute Maximum Ratings(Ta=25 °C)

Parameter	Symbol	Ratings	Unit
Power Supply Voltage	VDD	7.0	V
Power Dissipation	Pd	690 *1	mW
Storage Temperature Range	Tstg	-55 : * +150	°C
STBY Terminal Input Range	Vstby	-0.1:*VDD+0.1	V
IN+, IN- Terminal Input Range	Vin	-0.1:*VDD+0.1	V

\*1 When mounted on a 50 mmx58mm Rohm standard board, reduce by 5.52 mW/°C above Ta = +25 °C.

## h Operating Conditions

Parameter	Symbol	Ratings	Unit
Power Supply Voltage	VDD	+2.5 : * +5.5	V
Temperature Range	Topr	-40 : * +85	°C

\* This product is not designed for protection against radioactive rays

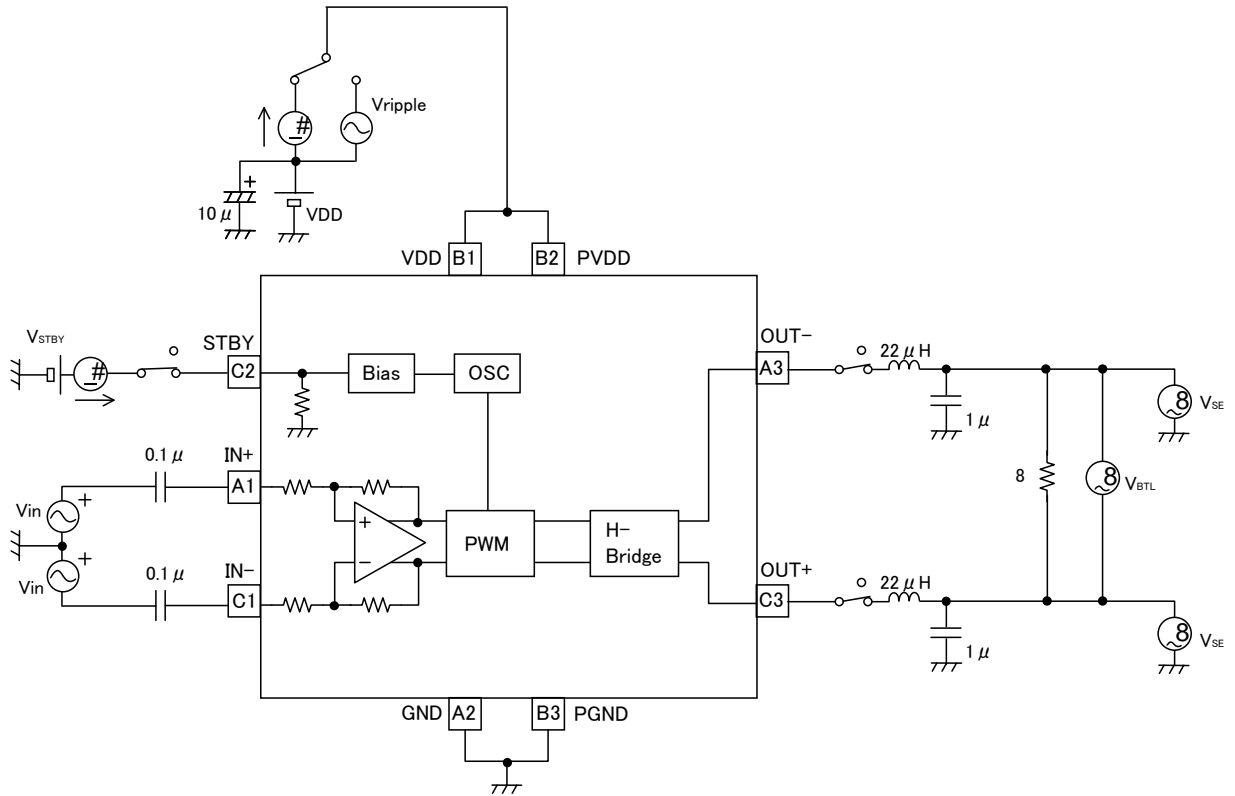
**h Electric Characteristics**

(Unless otherwise specified, Ta=25°, VDD=3.6V, f=1kHz, RL=8Ω, AC item=LC Filter; L=22μH, C=1μF)

Parameter	Symbol	Rating			Unit	Conditions	
		MIN.	TYP.	MAX.			
Circuit current (No signal)	I <sub>CC</sub>	Š	2.0	4.0	mA	Active mode, No load	
Circuit current (Standby)	I <sub>STBY</sub>	Š	0.1	2	A	Standby mode	
Output power 1	P <sub>O1</sub>	450	680	Š	mW	BTL, f=1kHz, THD+N=1% *1	
Output power 2	P <sub>O2</sub>	550	850	Š	mW	BTL, f=1kHz, THD+N=10% *1	
Voltage gain	G <sub>V</sub>	11.4	11.9	12.4	dB	BTL	
Power Supply Rejection Ratio	P <sub>SRR</sub>	45	53	Š	dB	BTL, f=1kHz, V <sub>ripple</sub> =0.1V <sub>pp</sub> *2	
Output offset voltage	ΔV <sub>o</sub>	-25	0	+25	mV	V <sub>in</sub> =0V, BTL	
Switching Frequency	f <sub>osc</sub>	175	250	325	kHz		
Start-up time	T <sub>on</sub>	0.39	0.51	0.73	msec		
Standby input Voltage	High-level	V <sub>STBYH</sub>	1.4	Š	VDD	V	Active mode
	Low-level	V <sub>STBYL</sub>	0	Š	0.4	V	Standby mode
Standby input current	High-level	I <sub>STBYH</sub>	6	12	18	A	V <sub>STBY</sub> =3.6V
	Low-level	I <sub>STBYL</sub>	-5	0	5	A	V <sub>STBY</sub> =0V

BTL=Bridged Tied Load (Voltage between A3-C3.), \*1;B.W.=400~\*30kHz,\*2;DIN AUDIO

**h Measurement Circuit Diagram**

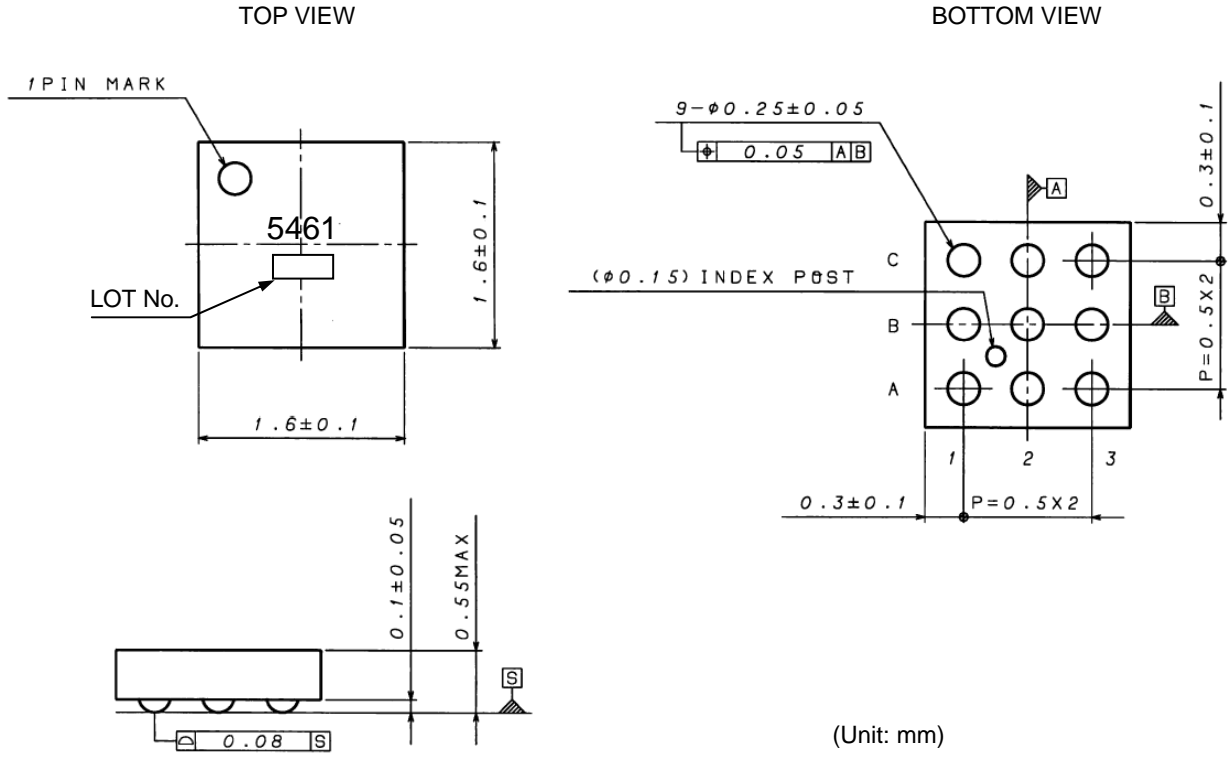


**h Active / Standby Control**

STBY Pin(C2pin)

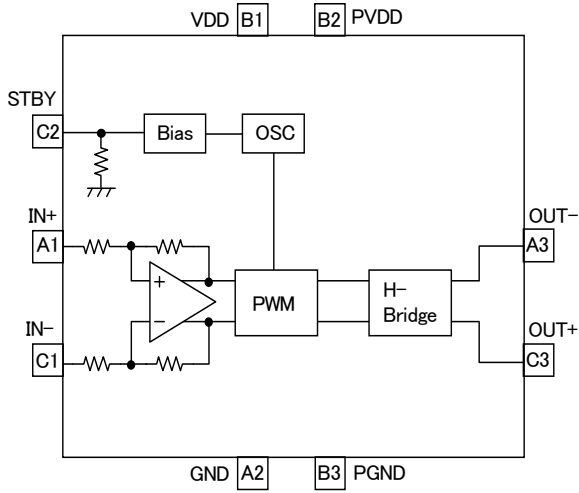
Mode	Pin level	Conditions
Active	H	IC active
Standby	L	IC shutdown

h Package Outlines



WL-CSP 9æ VCSP50L1

h Block Diagram



h Pin Assignment Chart

Pin No.	Pin Name
A1	IN+
A2	GND
A3	OUT-
B1	VDD
B2	PVDD
B3	PGND
C1	IN-
C2	STBY
C3	OUT+

h Application Circuit Example

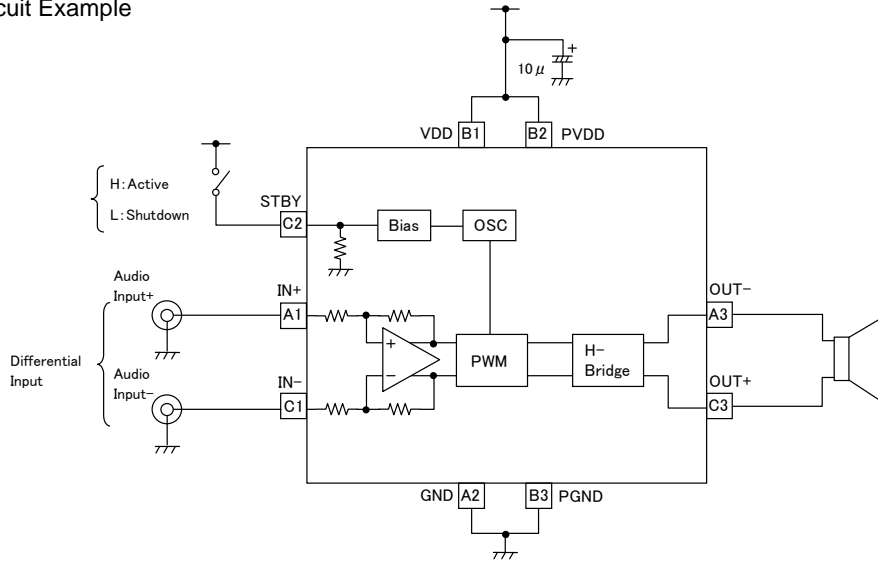


Fig.1 Differential input for mobile phone

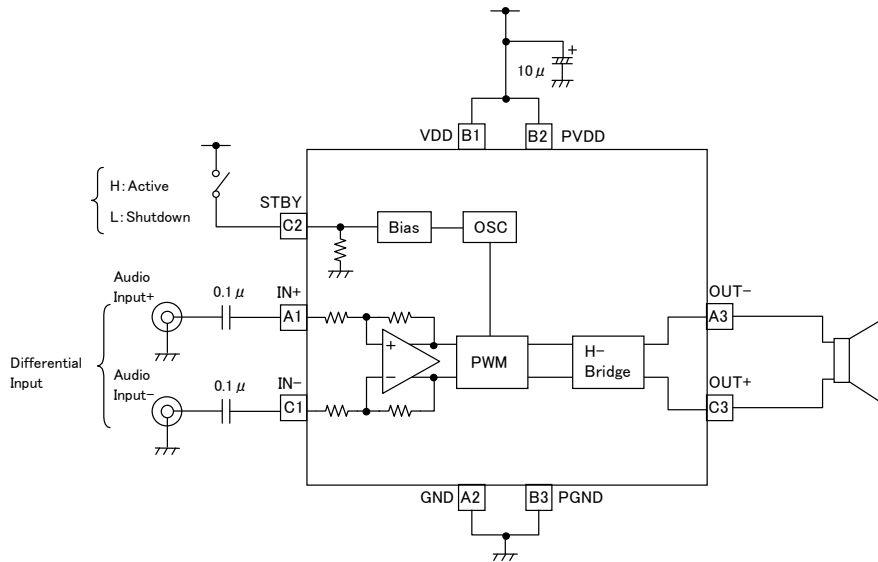


Fig.2 Differential input with coupling input capacitors

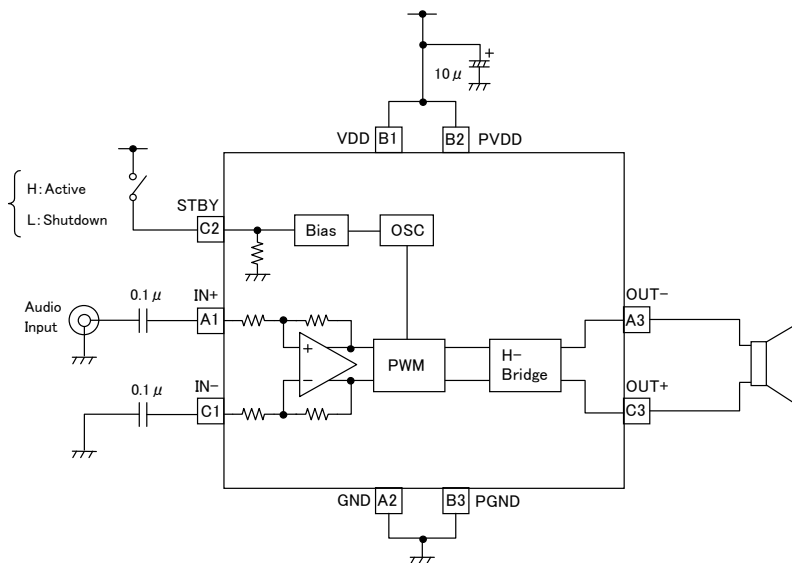
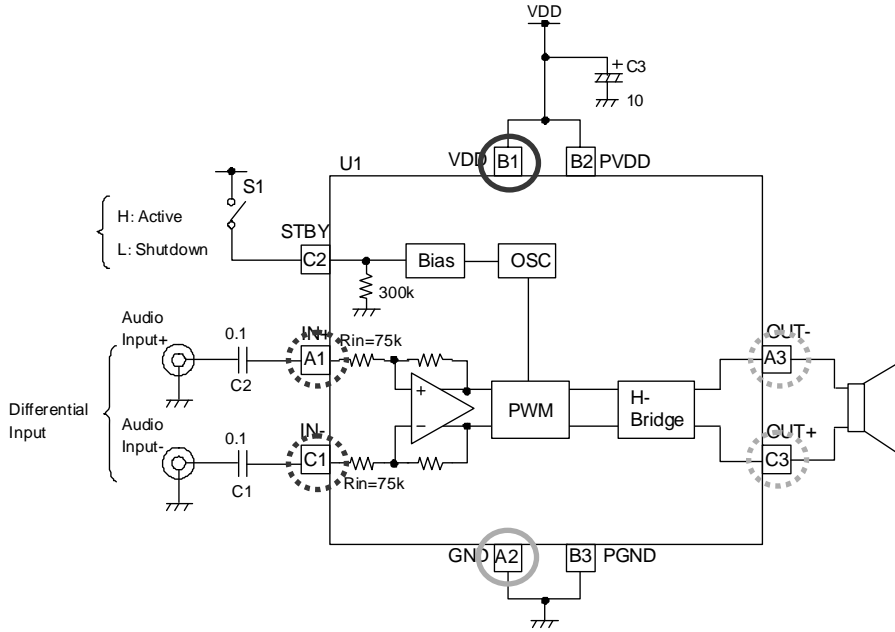


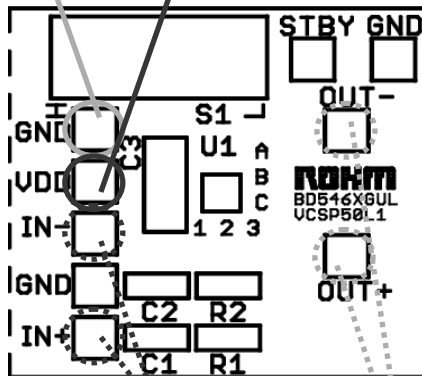
Fig.3 Single-Ended input

h Evaluation board Circuit Diagram



Please connect to GND line.

Please connect to Power Supply (VDD=+2.5~5.5V) line.



Please connect to Input Signal line.

Please connect to Speaker .

**h Evaluation board Parts List**

Qty.	Item	Description	SMD Size	Manufacturer/ Part Number
2	C1, C2	Capacitor, 0.1 F	0603	Murata GRM188R71C104KA01D
1	C3	Capacitor, 10 F	A (3216)	ROHM TCFGA1A106M8R
1	S1	Slide Switch	4mm X 10.2mm	NKK SS-12SDP2
1	U1	IC, BD5461GUL, Mono Class-D audio amplifier	1.6mm X 1.6mm WL-CSP Package	ROHM BD5461GUL
1	PCB1	Printed-circuit board, BD5460GUL EVM	Š	Š

**h Description of External components**

Input coupling capacitor (C1,C2)

It makes a Input coupling capacitor 0.1μF.

Input impedance is 75k (Typ.).

It sets cutoff frequency  $f_c$  by the following formula by input coupling capacitor  $C1(=C2)$  and input impedance  $R_i$ .

$$f_c = \frac{1}{2\pi R_i C_1} [\text{Hz}]$$

In case of  $R_i=75k$  ,  $C_1(=C_2)=0.1\mu\text{F}$ , it becomes  $f_c =$  about 21 Hz.

Power decoupling capacitor (C3)

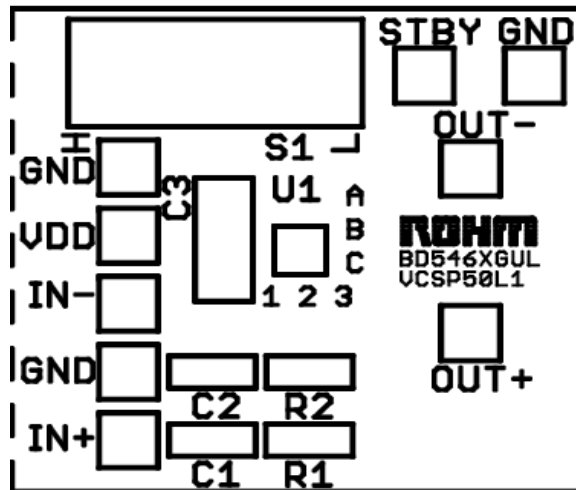
It makes a power decoupling capacitor 10 μF.

When making capacitance of the power decoupling capacitor, there is an influence in the Audio characteristic.

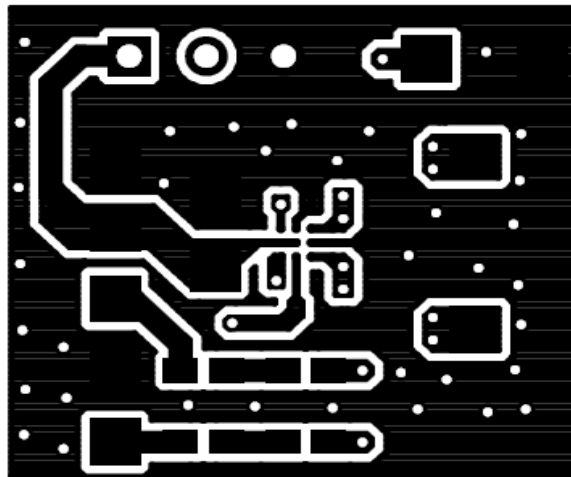
When making small, careful for the Audio characteristic at the actual application.

h Evaluation board PCB layer

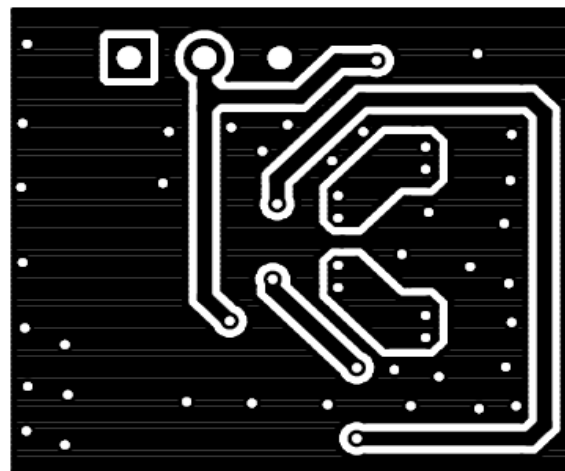
TOP Layer silk pattern



TOP Layer

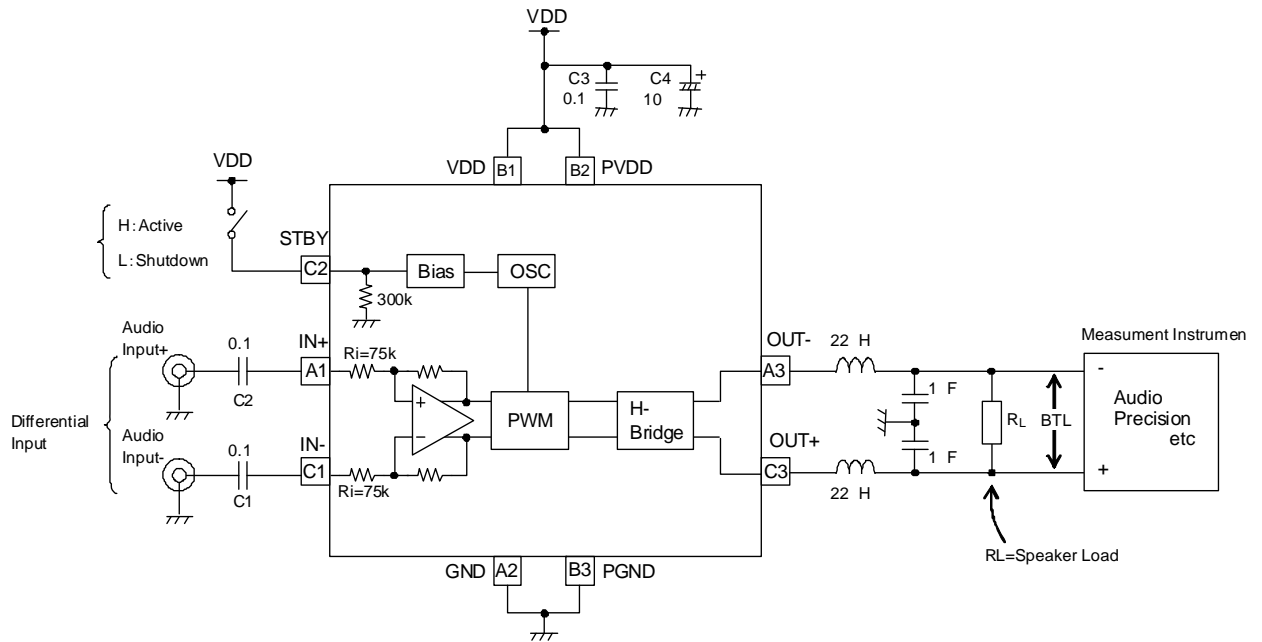


Bottom Layer



h The way of evaluating Audio characteristic

Evaluation Circuit Diagram



When measuring Audio characteristics, insert LC filter during the output terminal of IC and the speaker load and measure it. Arrange LC filter as close as possible to the output terminal of IC. In case of L=22 H, C=1 F, the cutoff frequency becomes the following.

$$f_c = \frac{1}{2 \sqrt{LC}} = \frac{1}{2 \sqrt{22 \text{ H} \times 1 \text{ F}}} \cong 34 \text{ kHz}$$

Use a big current type - Inductor L.  
(Reference)

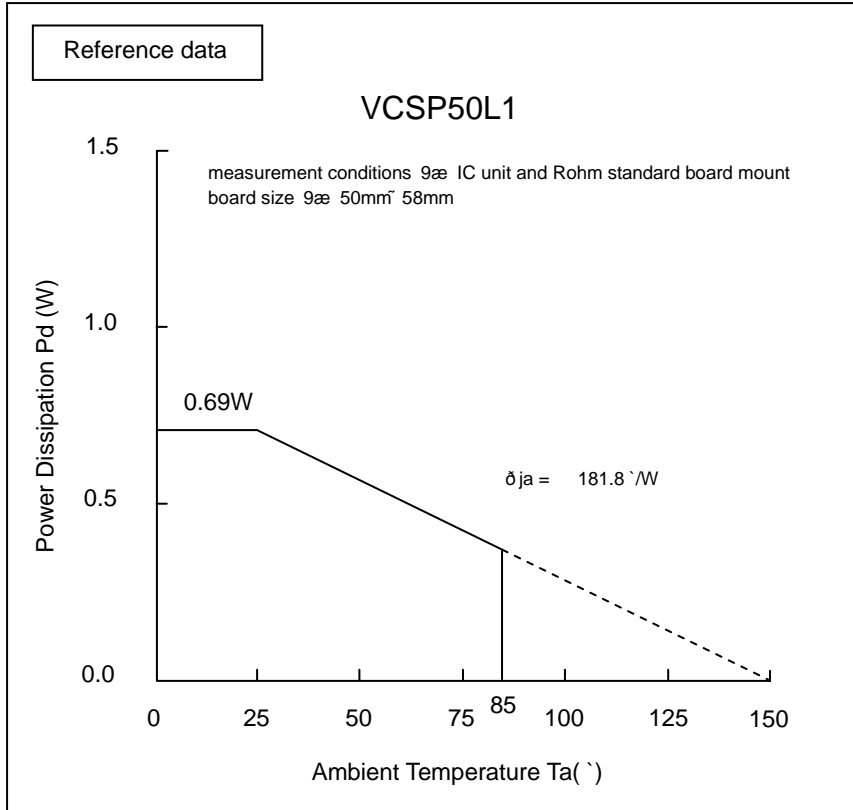
TDK: SLF12575T-220M4R0



h About the thermal design by the IC

Characteristics of an IC have a great deal to do with the temperature at which it is used, and exceeding absolute maximum ratings may degrade and destroy elements. Careful consideration must be given to the heat of the IC from the two standpoints of immediate damage and long-term reliability of operation. Pay attention to points such as the following. Since an maximum junction temperature ( $T_{jMAX.}$ ) or operating temperature range ( $T_{opr}$ ) is shown in the absolute maximum ratings of the IC, to reference the value, find it using the Pd-Ta characteristic (temperature derating curve). If an input signal is too great when there is insufficient radiation, TSD (thermal shutdown) may operate. TSD, which operates at a chip temperature of approximately  $+180^{\circ}$ , is canceled when this goes below approximately  $+100^{\circ}$ . Since TSD operates persistently with the purpose of preventing chip damage, be aware that long-term use in the vicinity that TSD affects decrease IC reliability.

Temperature Derating Curve



Note) Values are actual measurements and are not guaranteed.

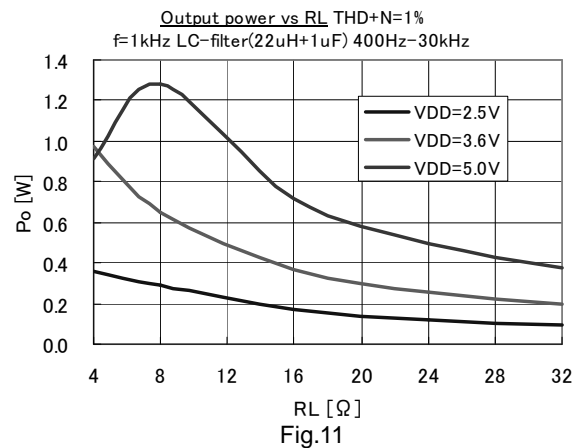
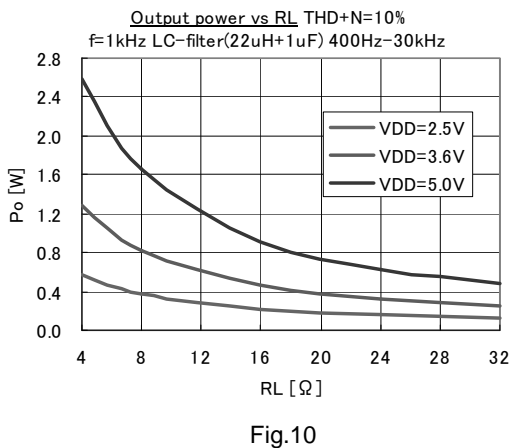
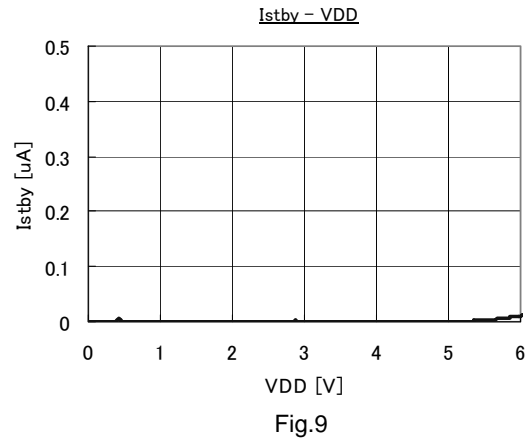
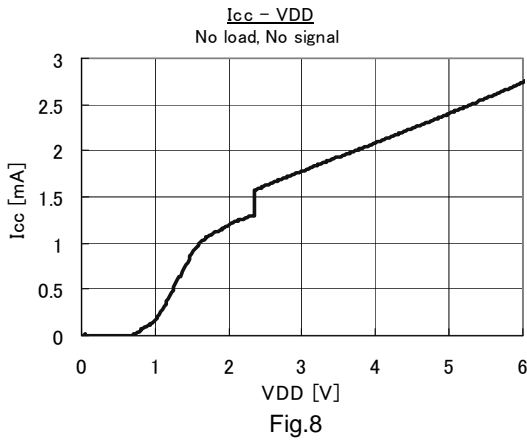
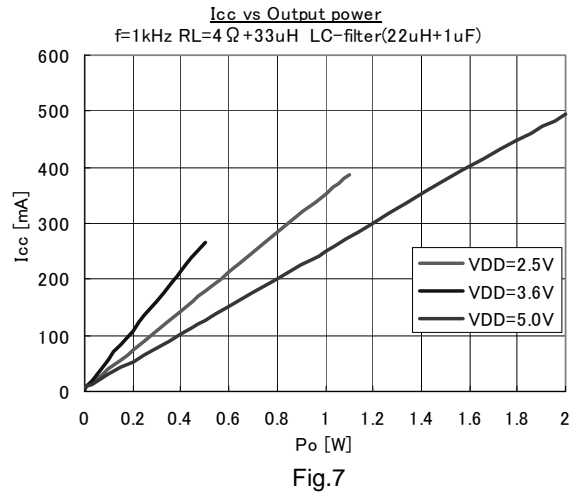
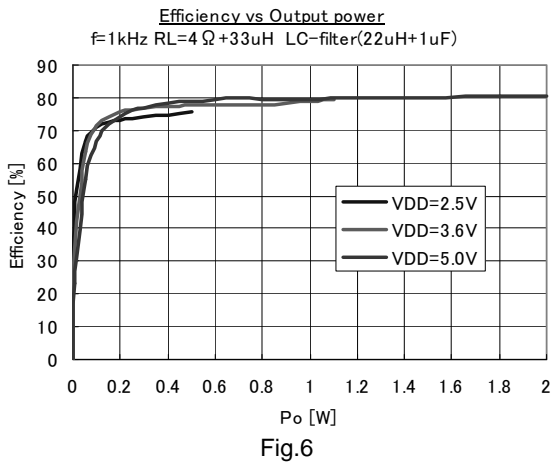
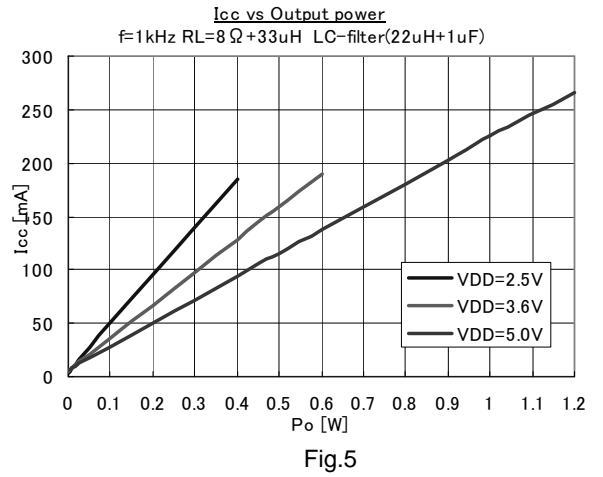
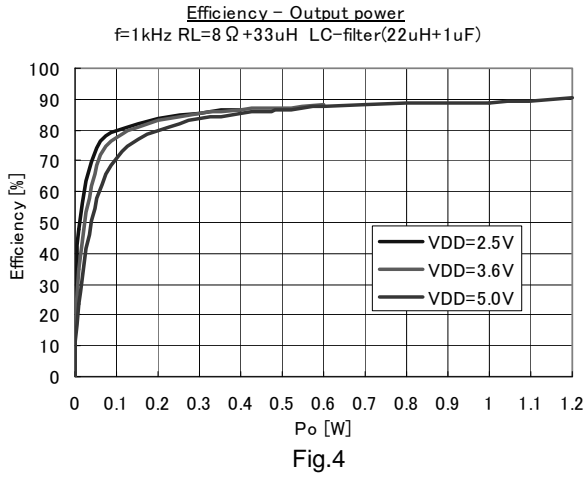
Power dissipation values vary according to the board on which the IC is mounted. The Power dissipation of this IC when mounted on a multilayer board designed to radiate is greater than the values in the graph above.

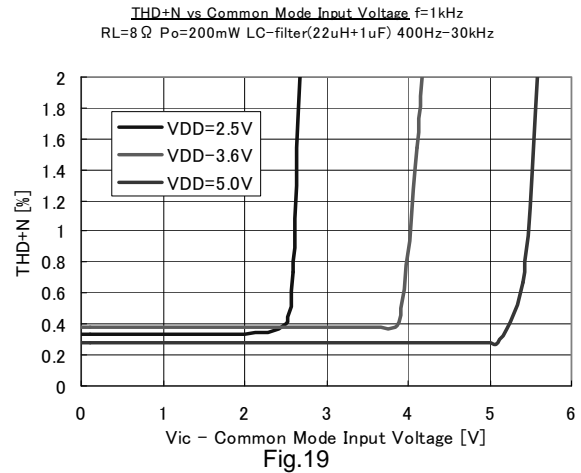
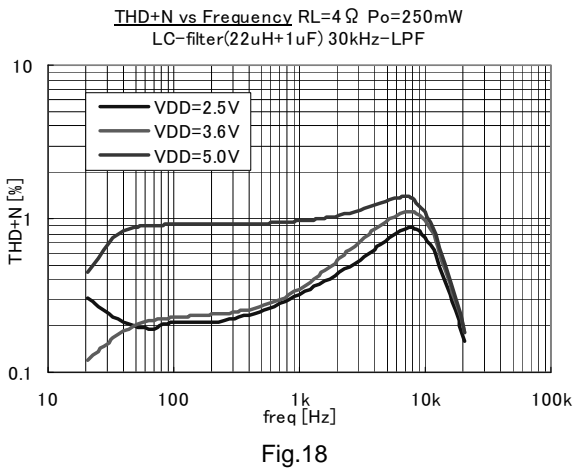
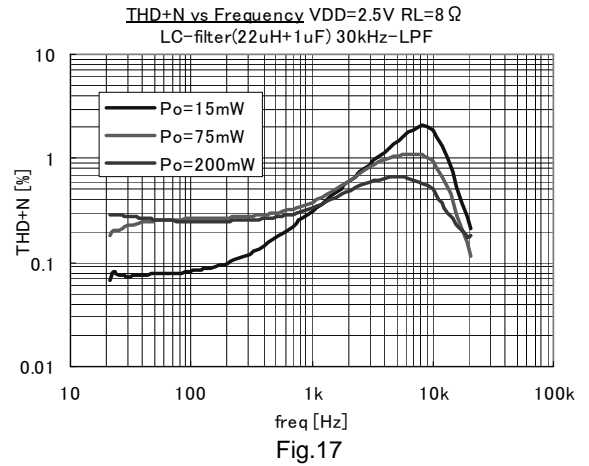
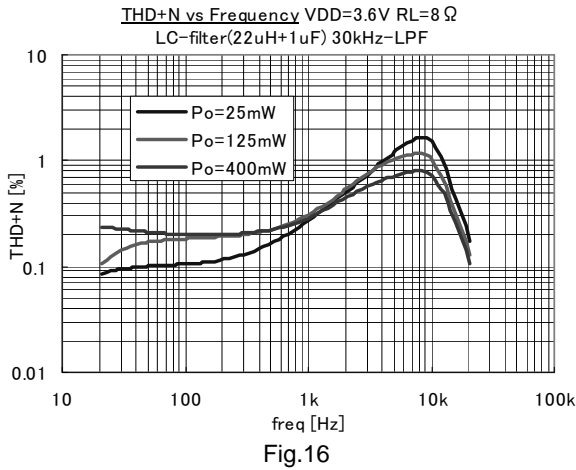
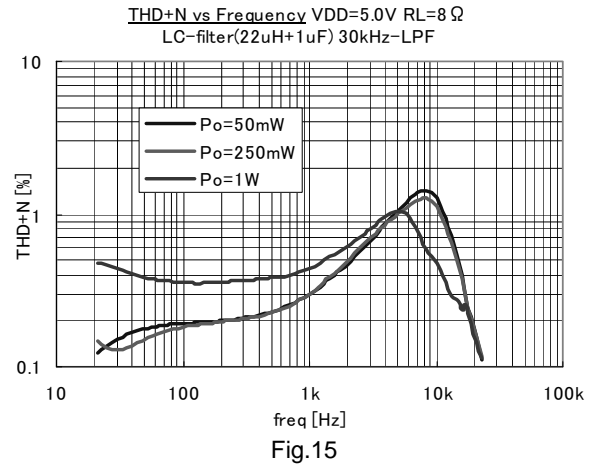
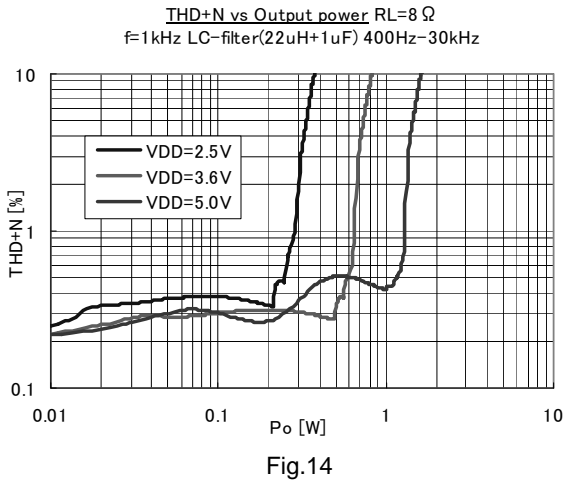
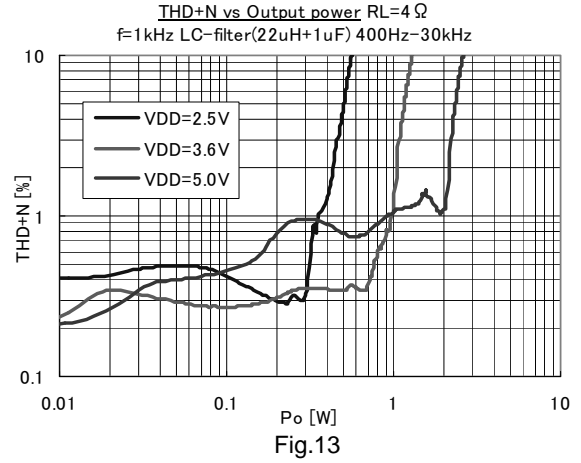
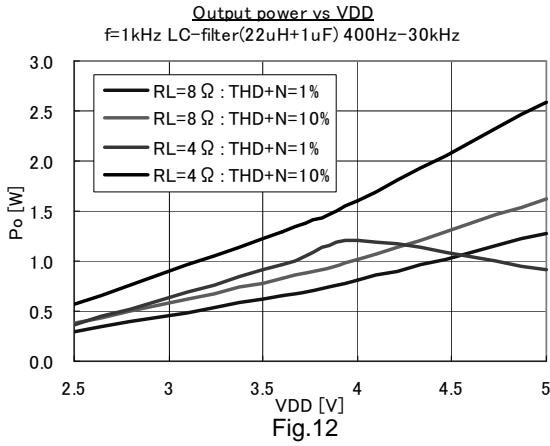
## h Typical Characteristics

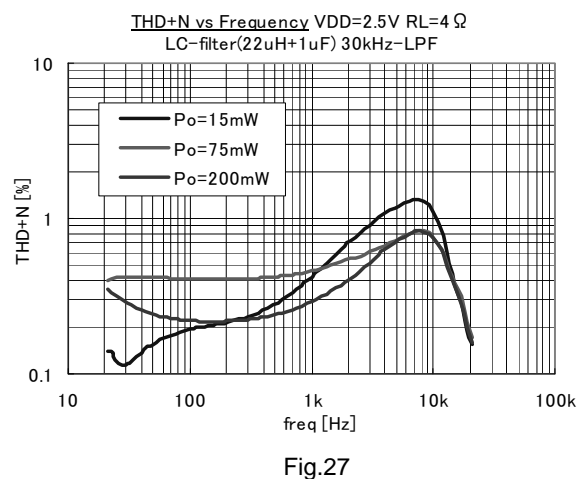
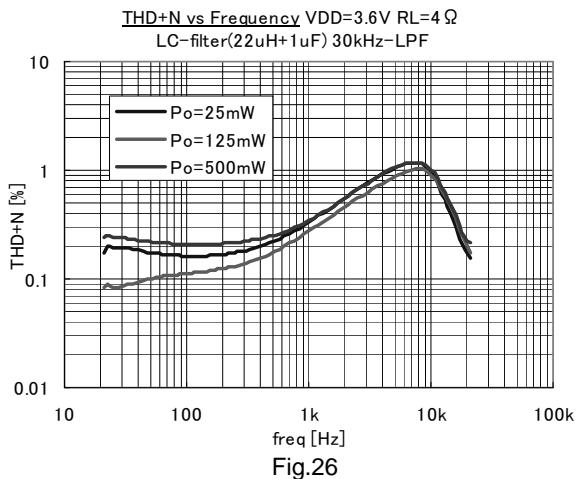
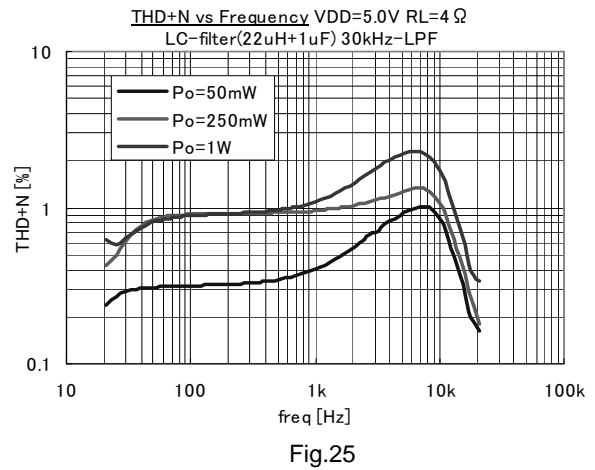
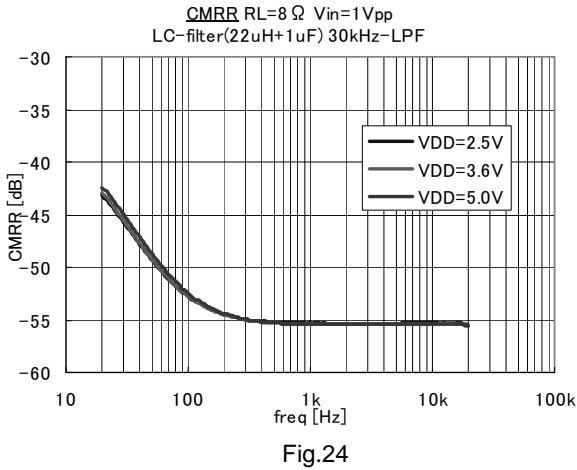
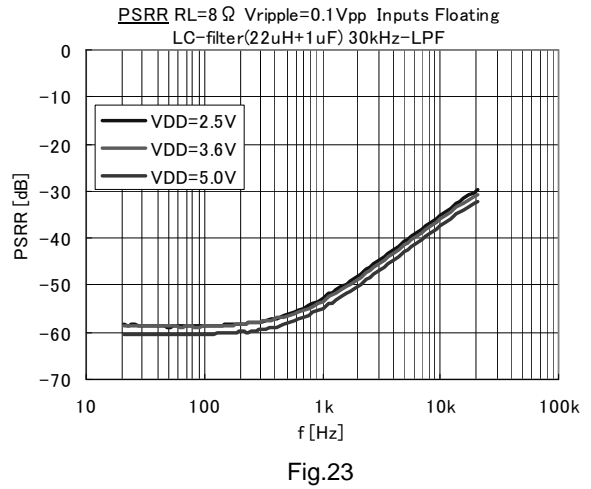
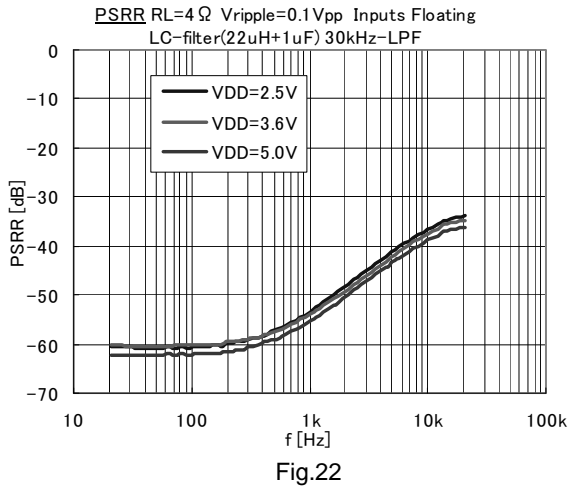
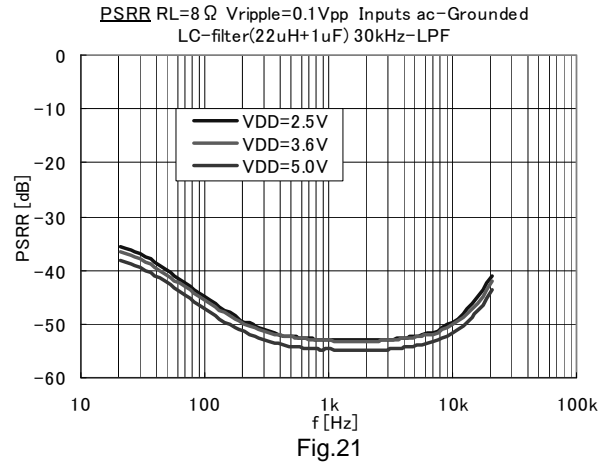
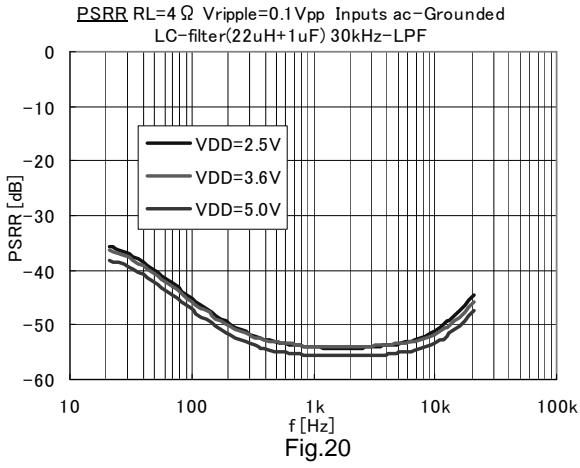
Table of graphs

Items	Parameter	Figure
Efficiency	vs Output power	4, 6
Supply current (I <sub>cc</sub> )	vs Output power	5, 7
	vs Supply voltage	8
Shutdown current (I <sub>stby</sub> )	vs Supply voltage	9
Output power (P <sub>o</sub> )	vs Load resistance	10, 11
	vs Supply voltage	12
Total harmonic distortion plus noise (THD+N)	vs Output power	13, 14
	vs Frequency	15, 16, 17, 18, 25, 26, 27
	vs Common-mode input voltage	19
Supply voltage rejection ratio (PSRR)	vs Frequency	20, 21, 22, 23
Common-mode rejection ratio (CMRR)	vs Frequency	24
Gain	vs Frequency	28, 29

h Reference Data







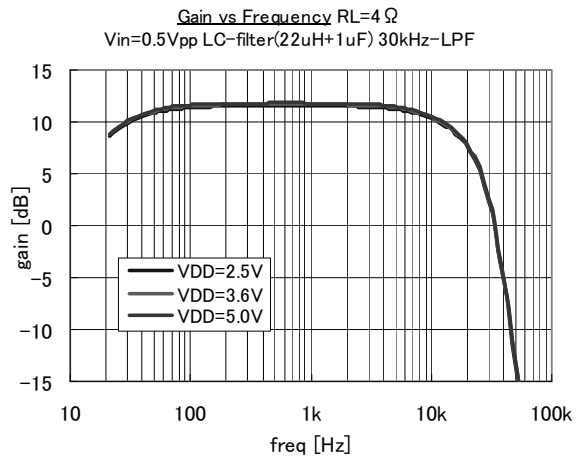


Fig.28

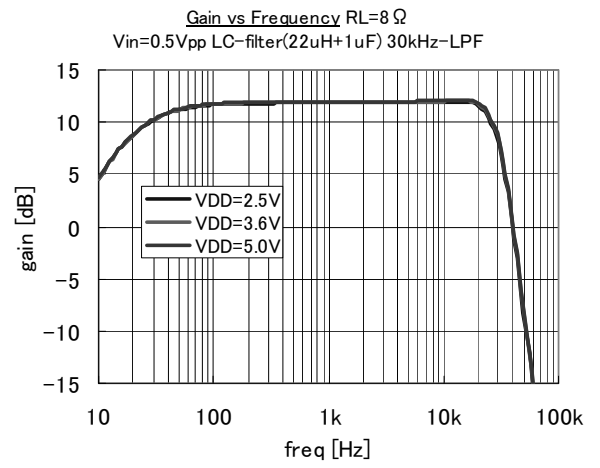


Fig.29







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